

FIG. 1

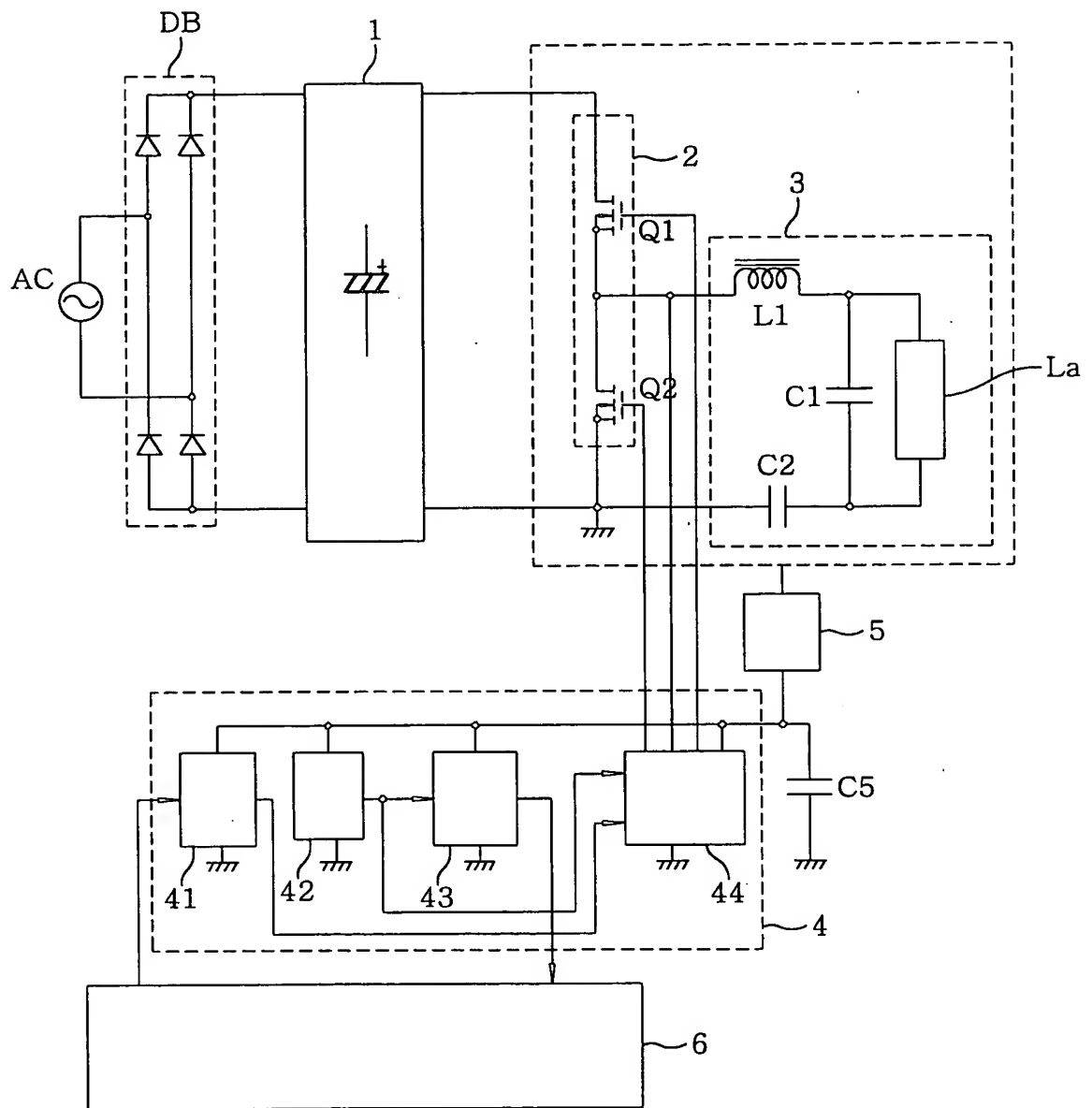


FIG. 2

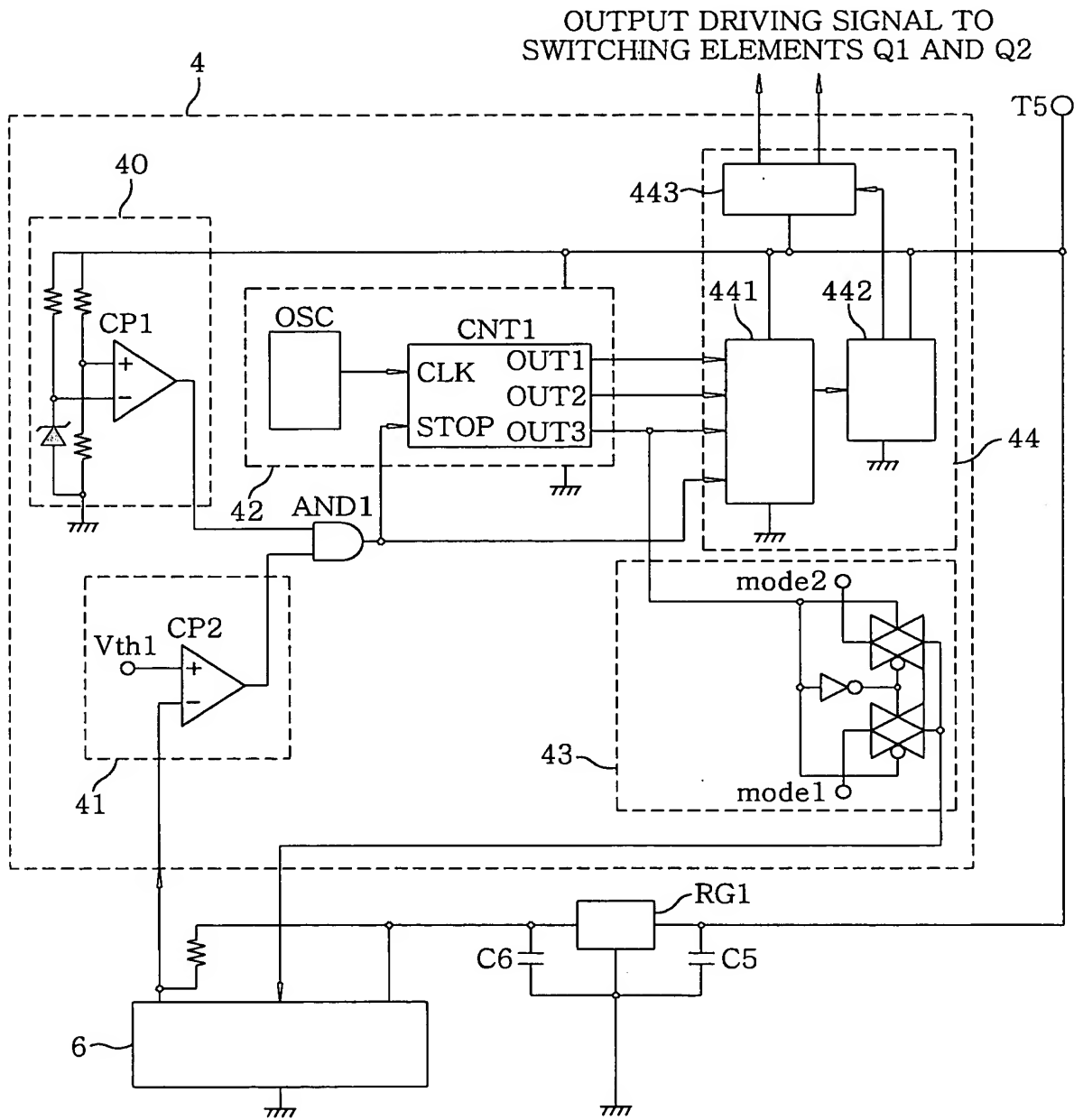


FIG. 3

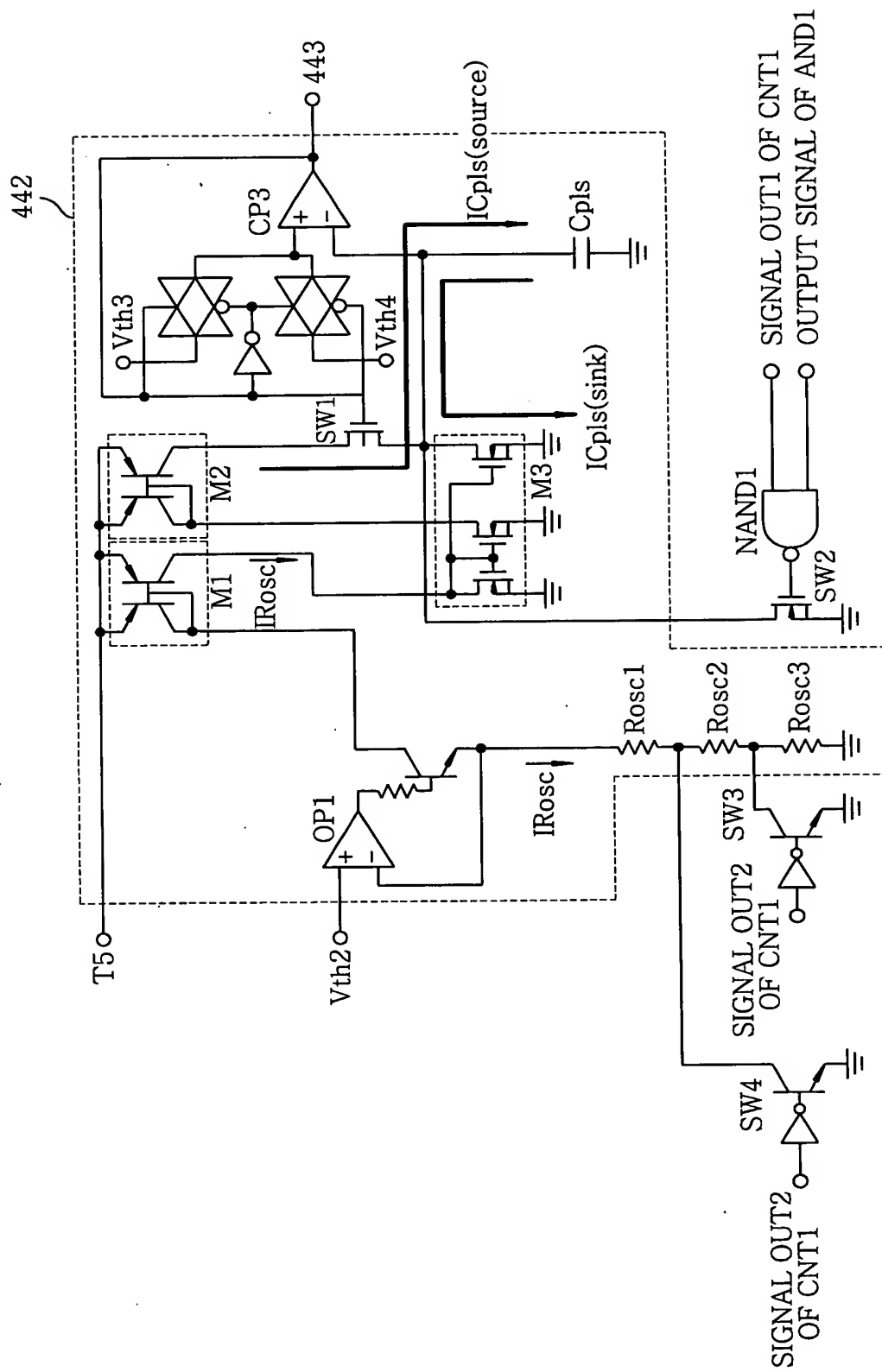


FIG. 4

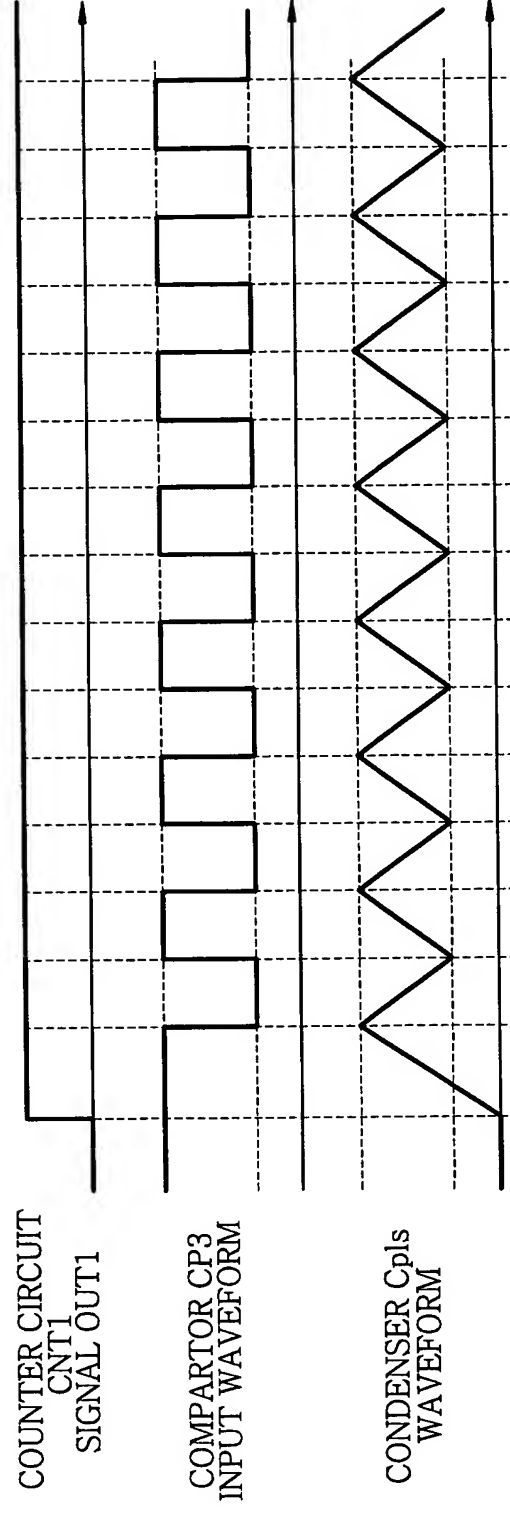


FIG. 5

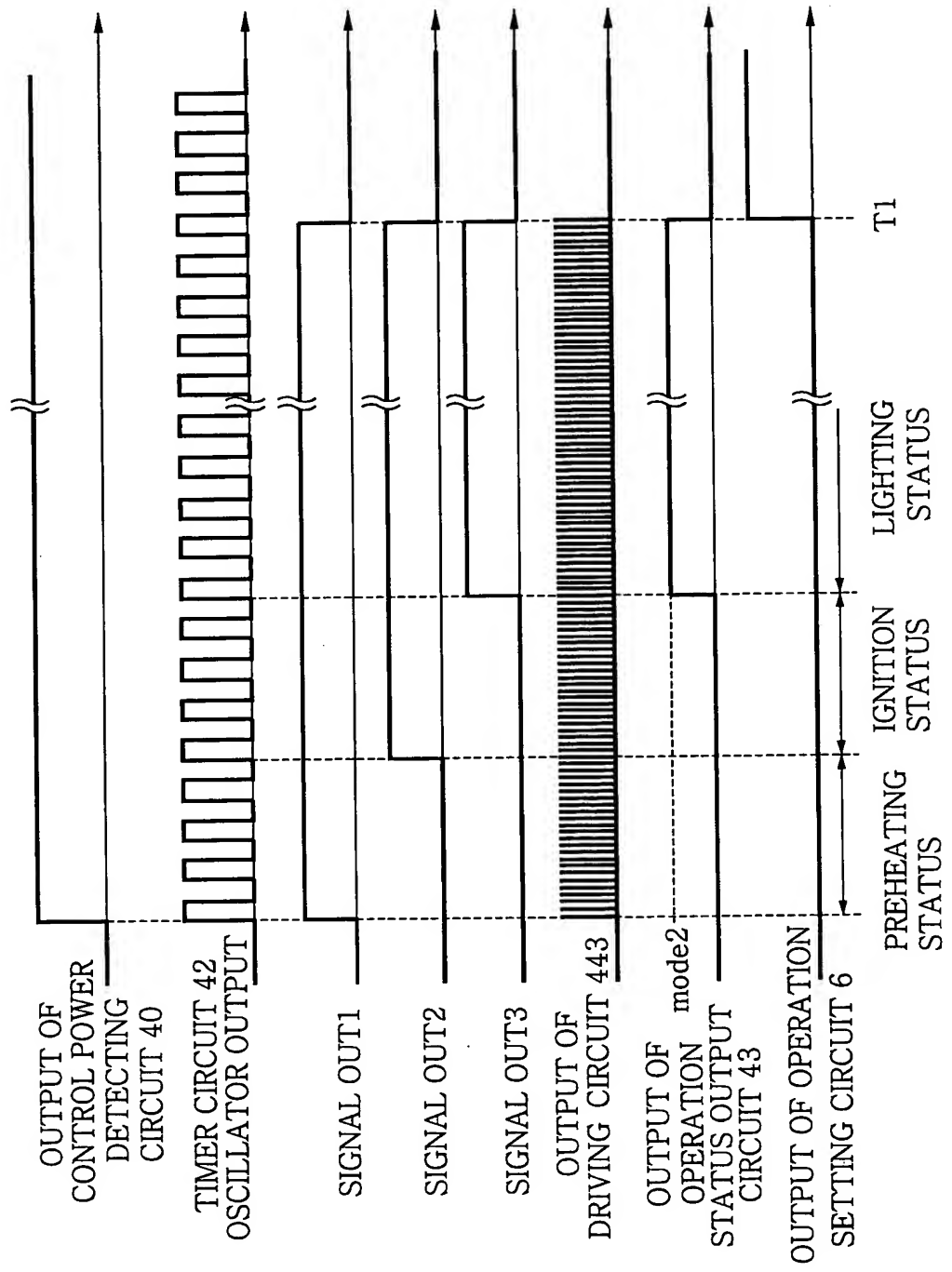


FIG. 6

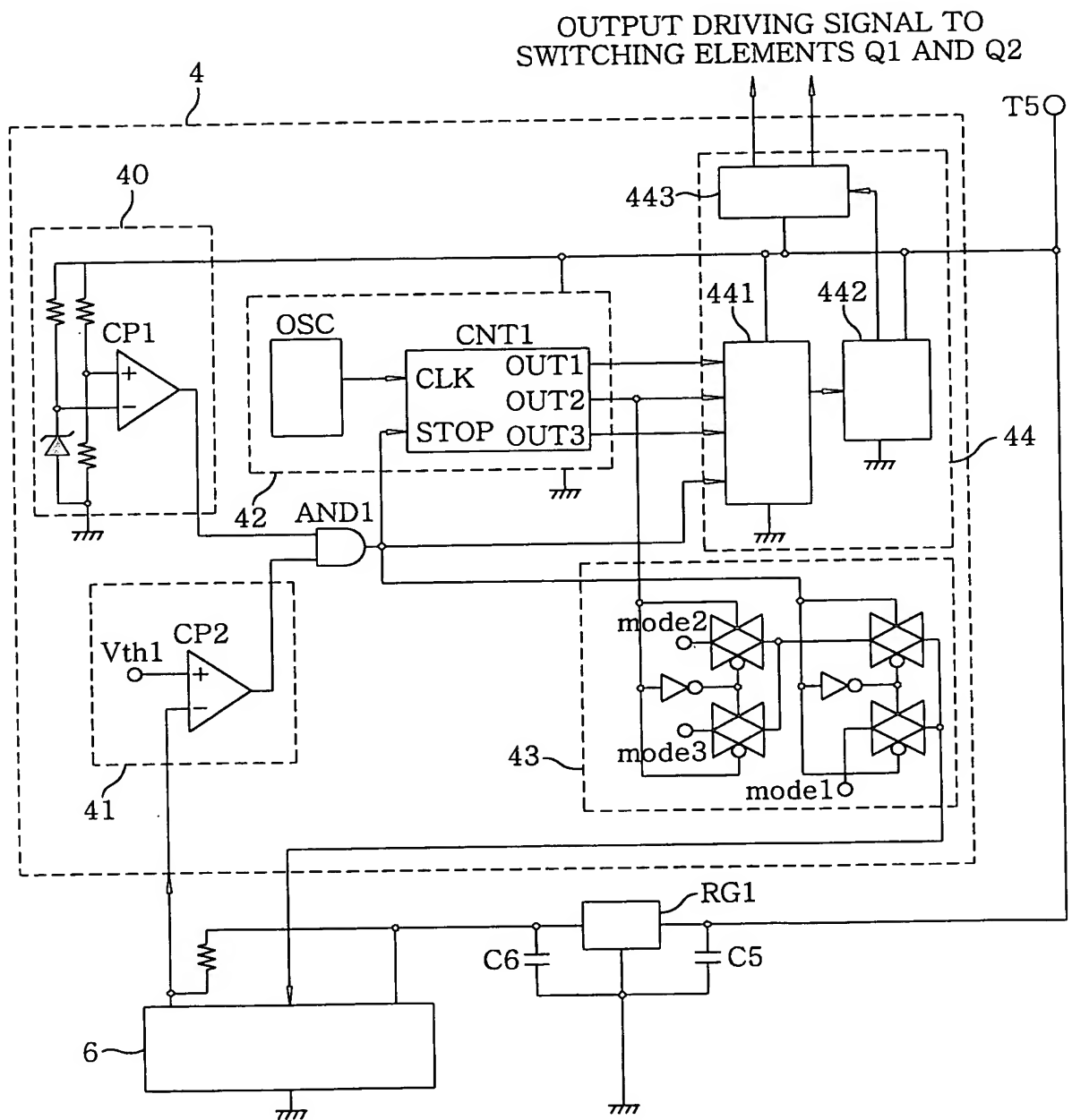


FIG. 7

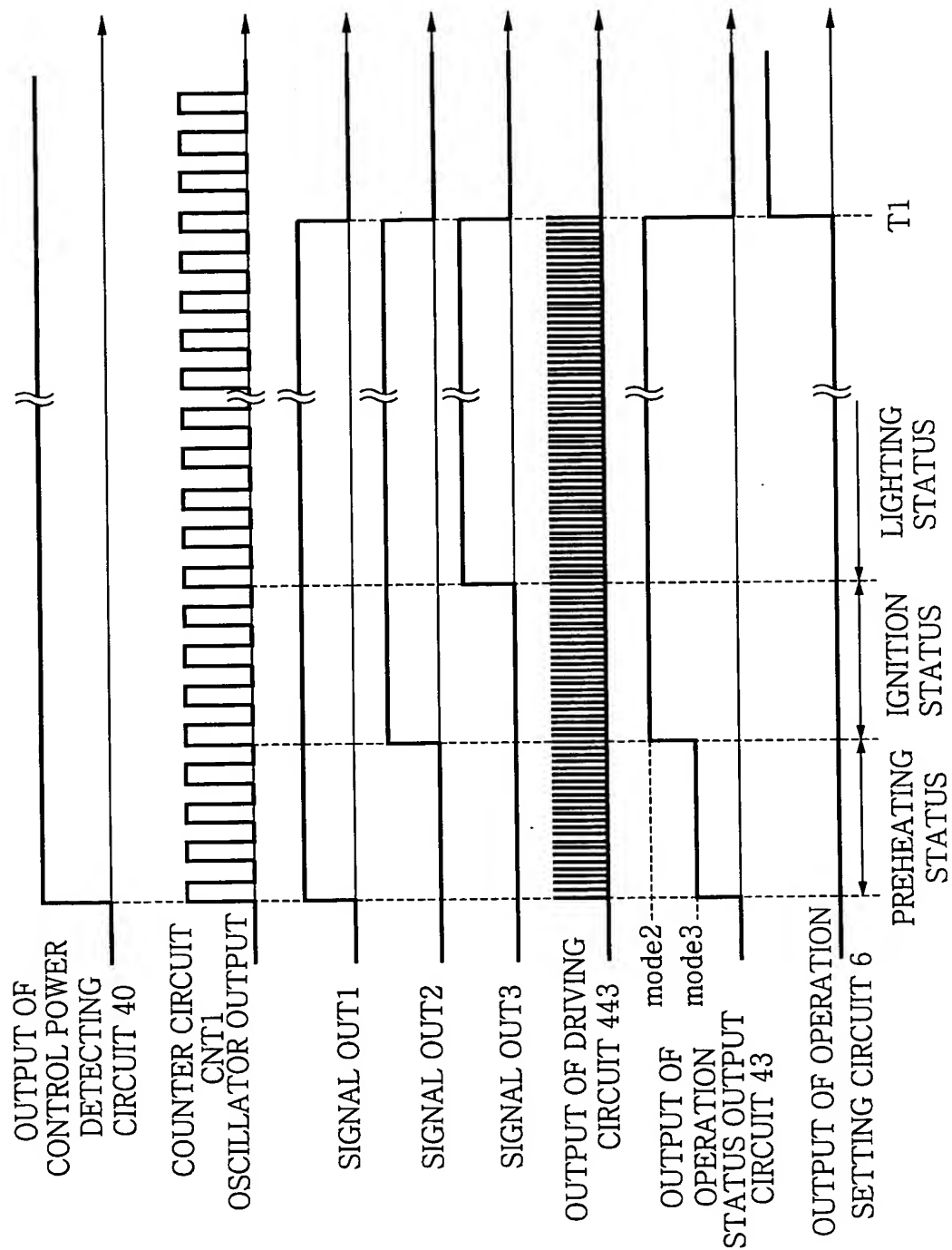


FIG. 8

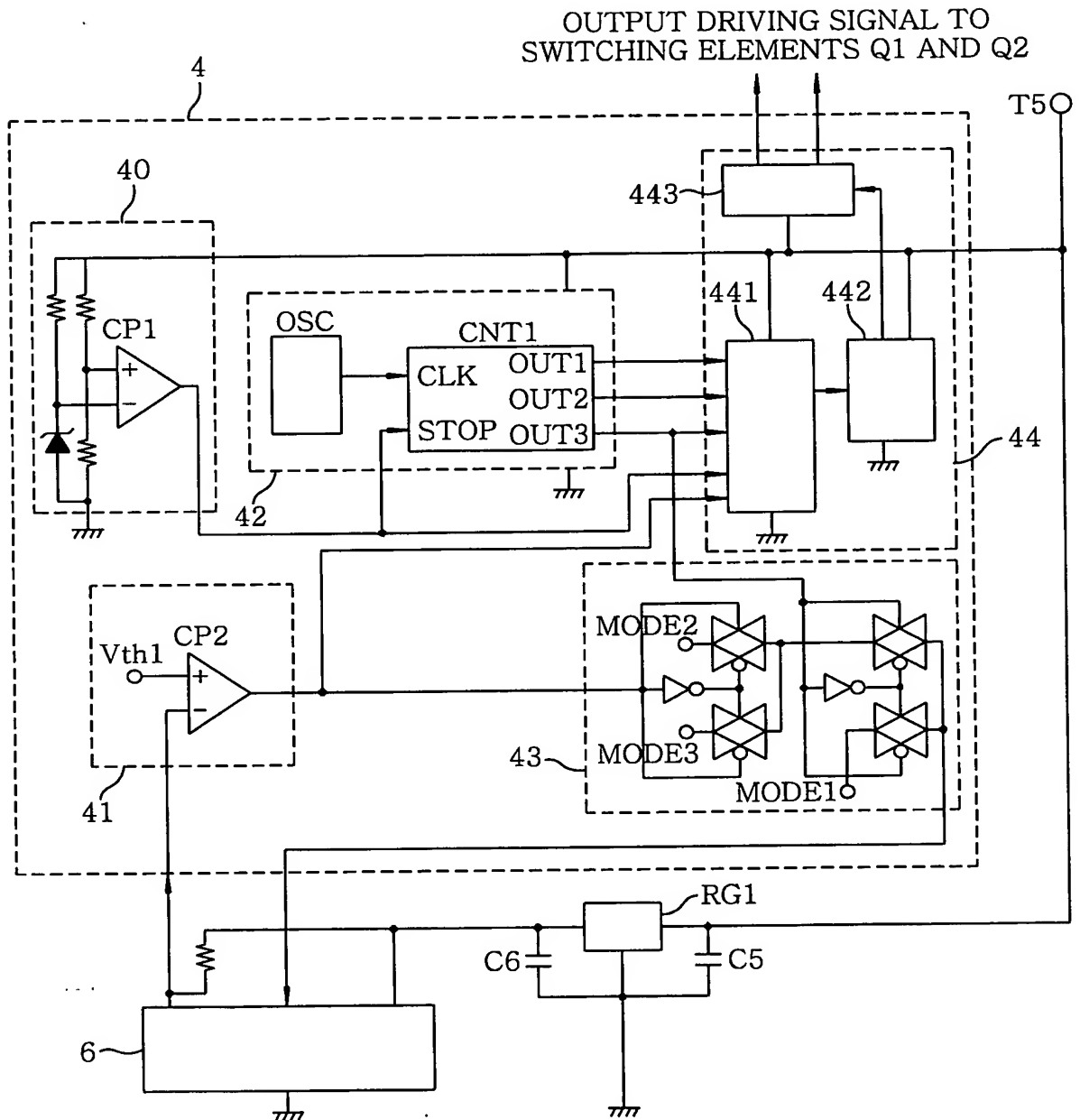


FIG. 9

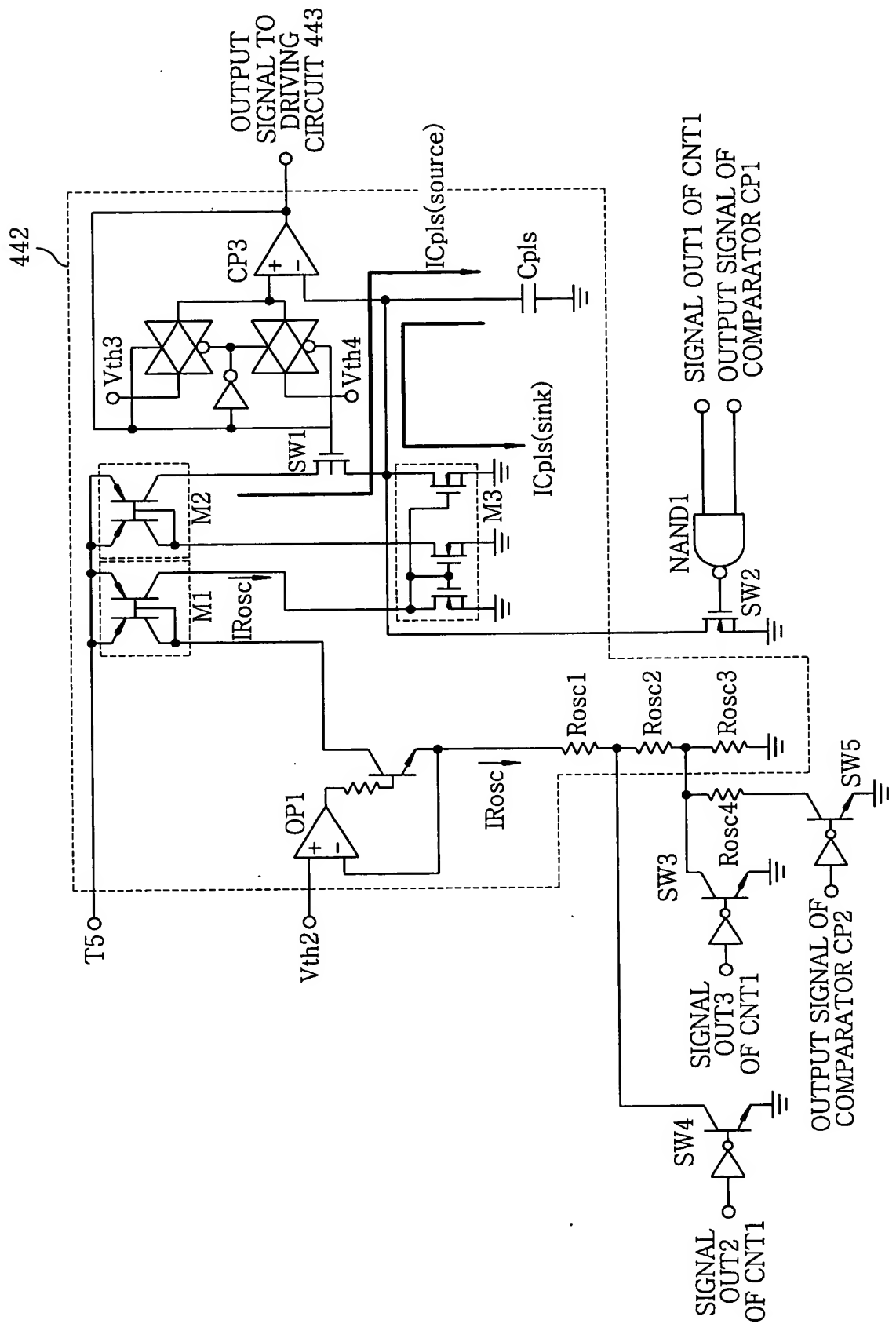


FIG. 10

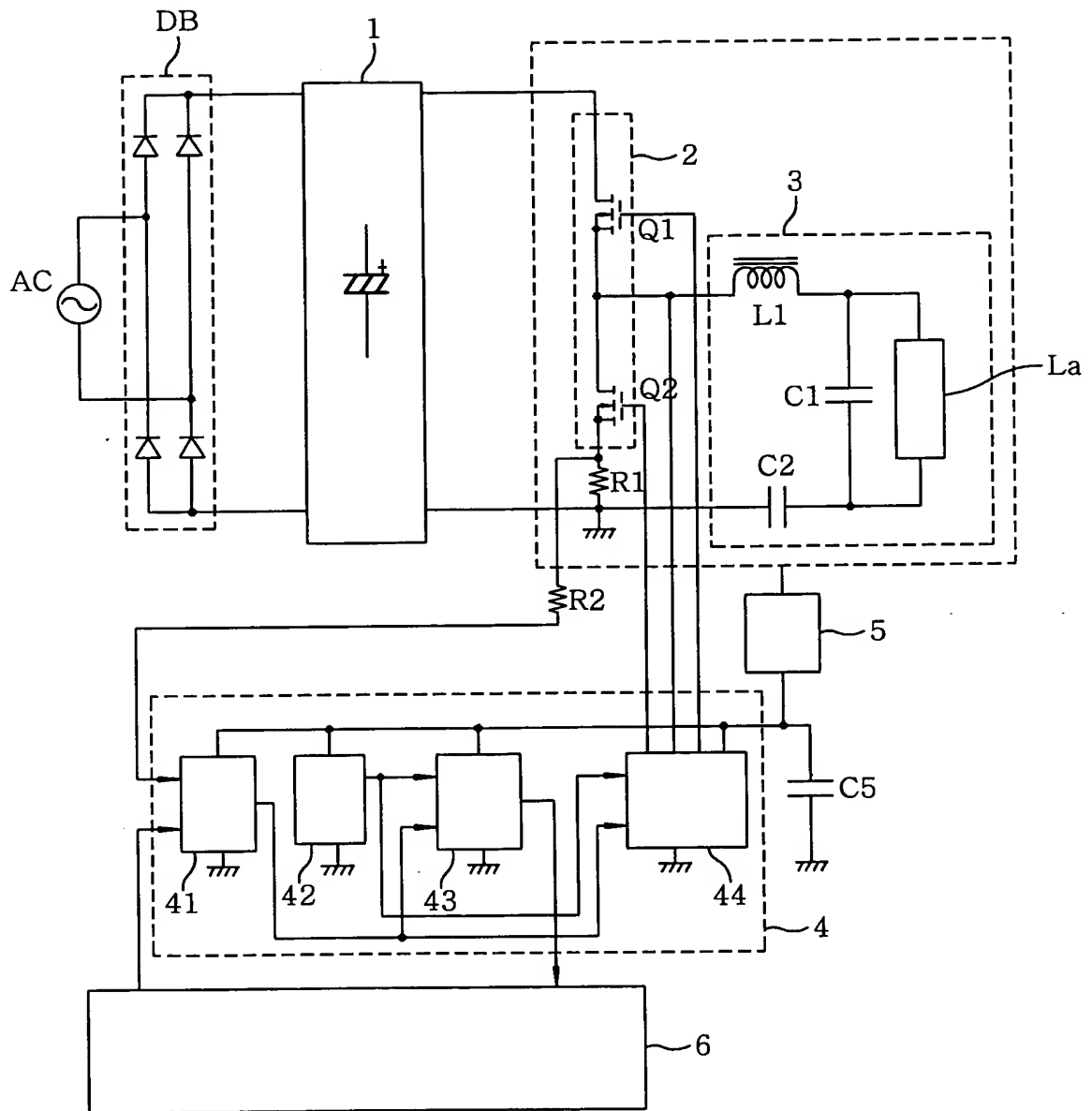


FIG. 11

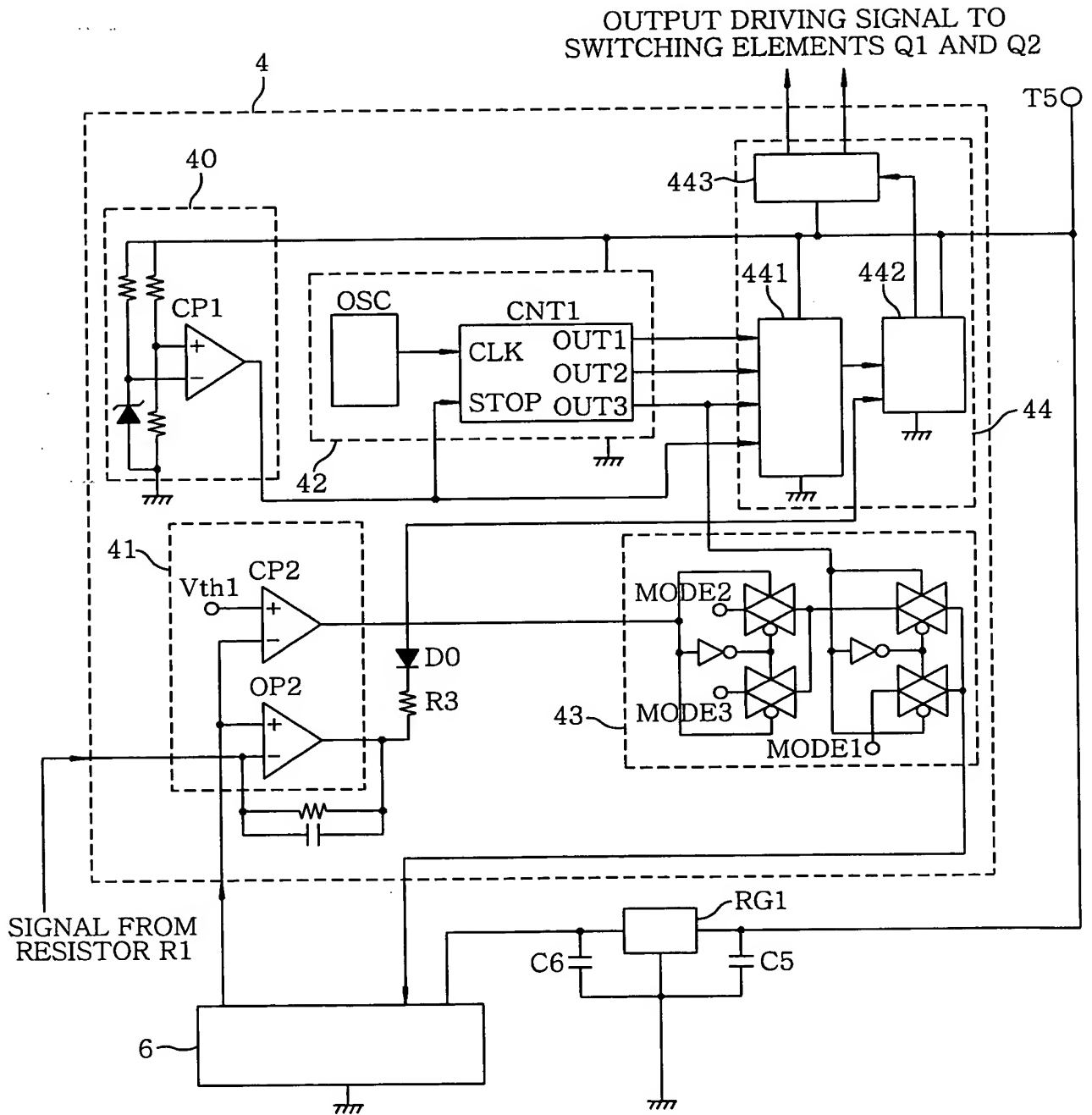


FIG. 12

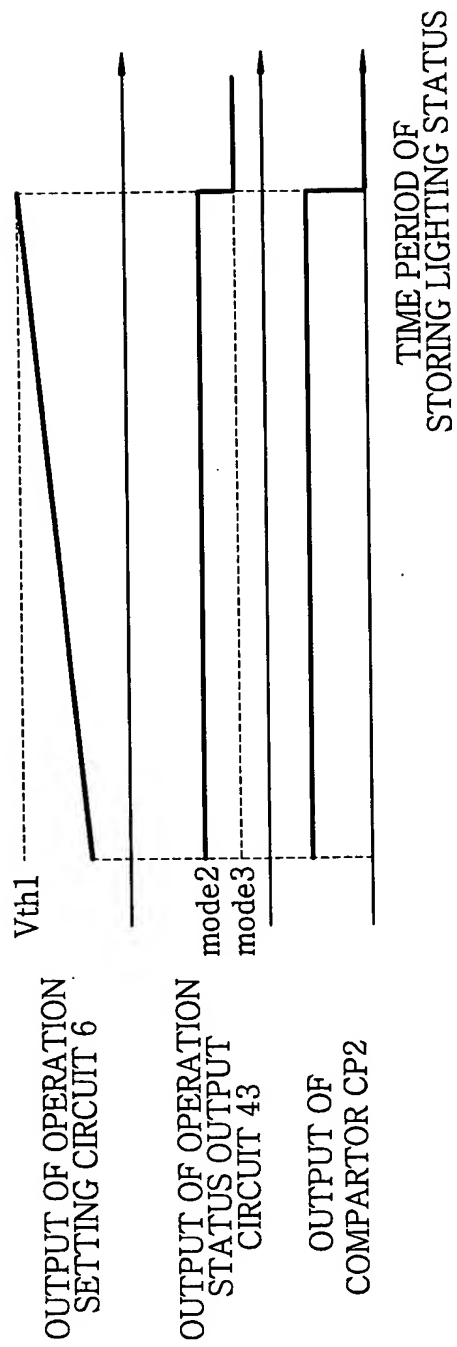


FIG. 13

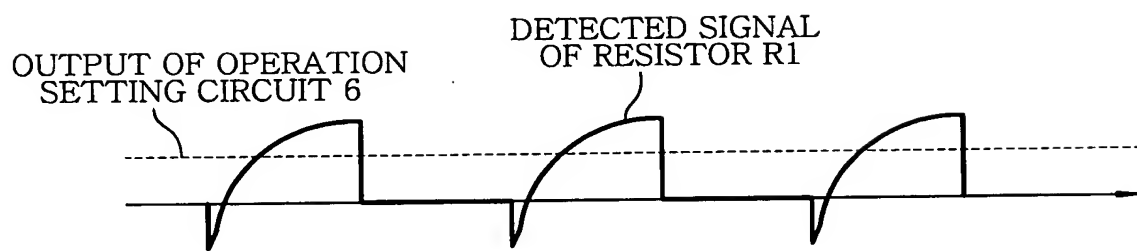


FIG. 14

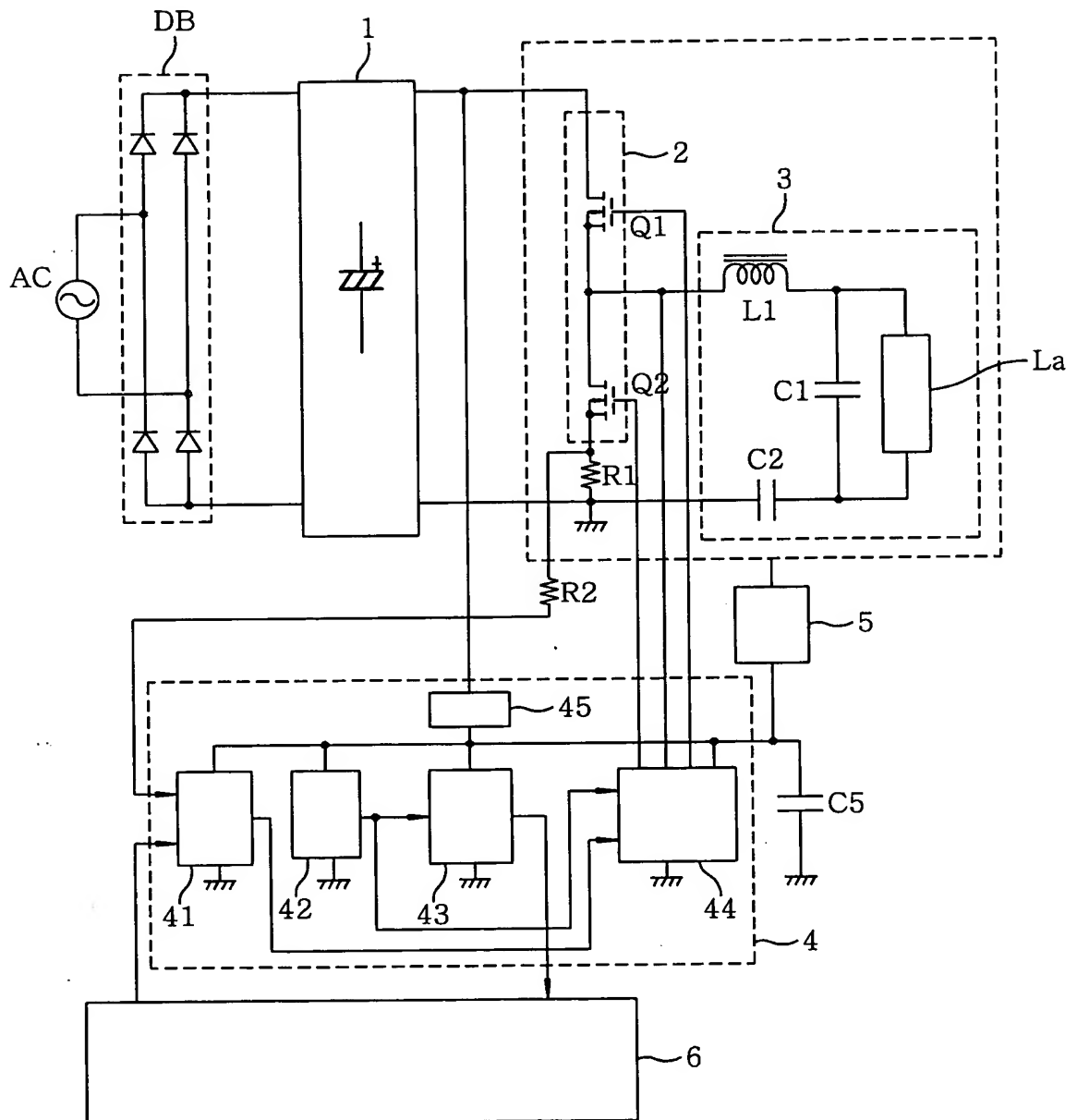


FIG. 15

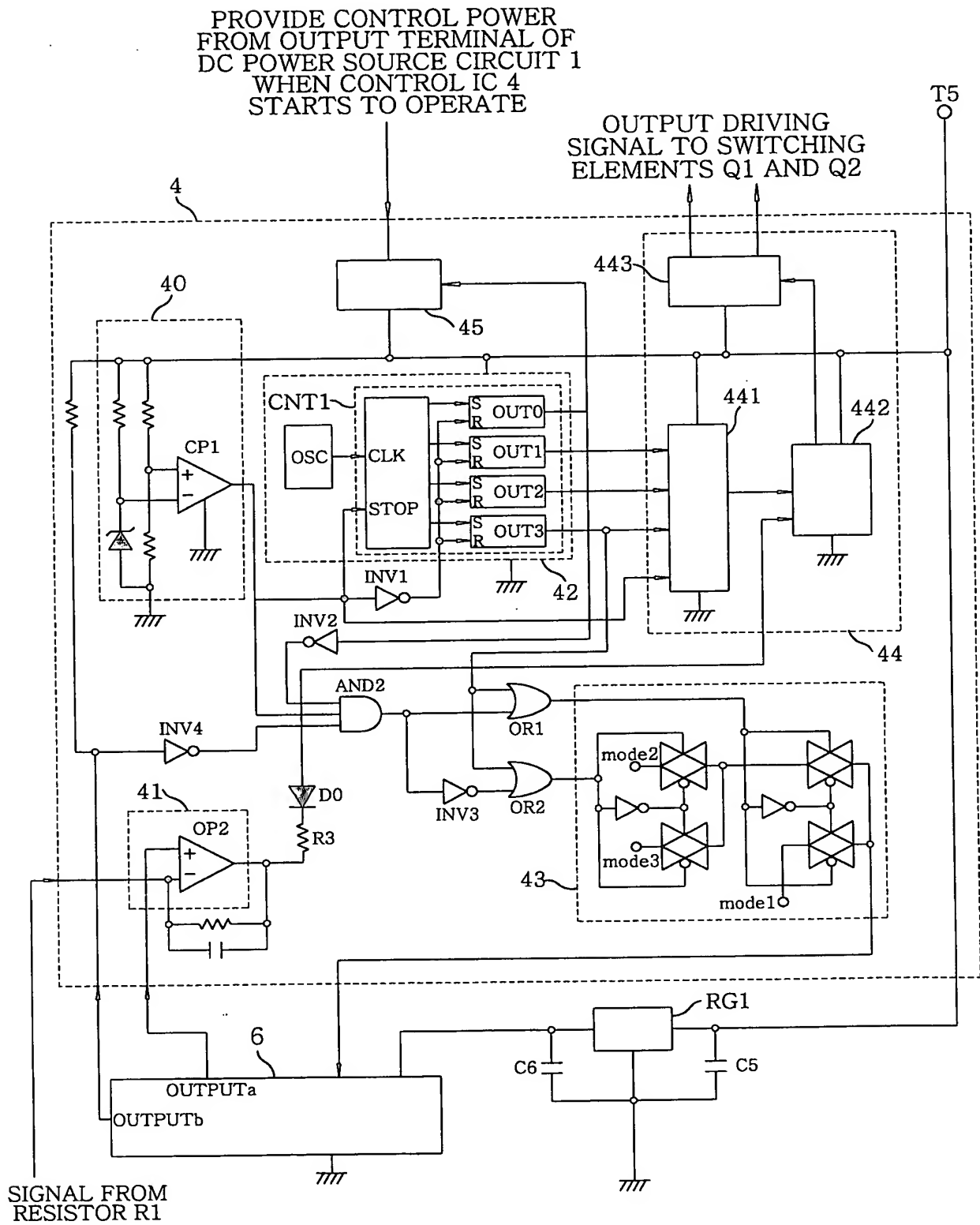


FIG. 16

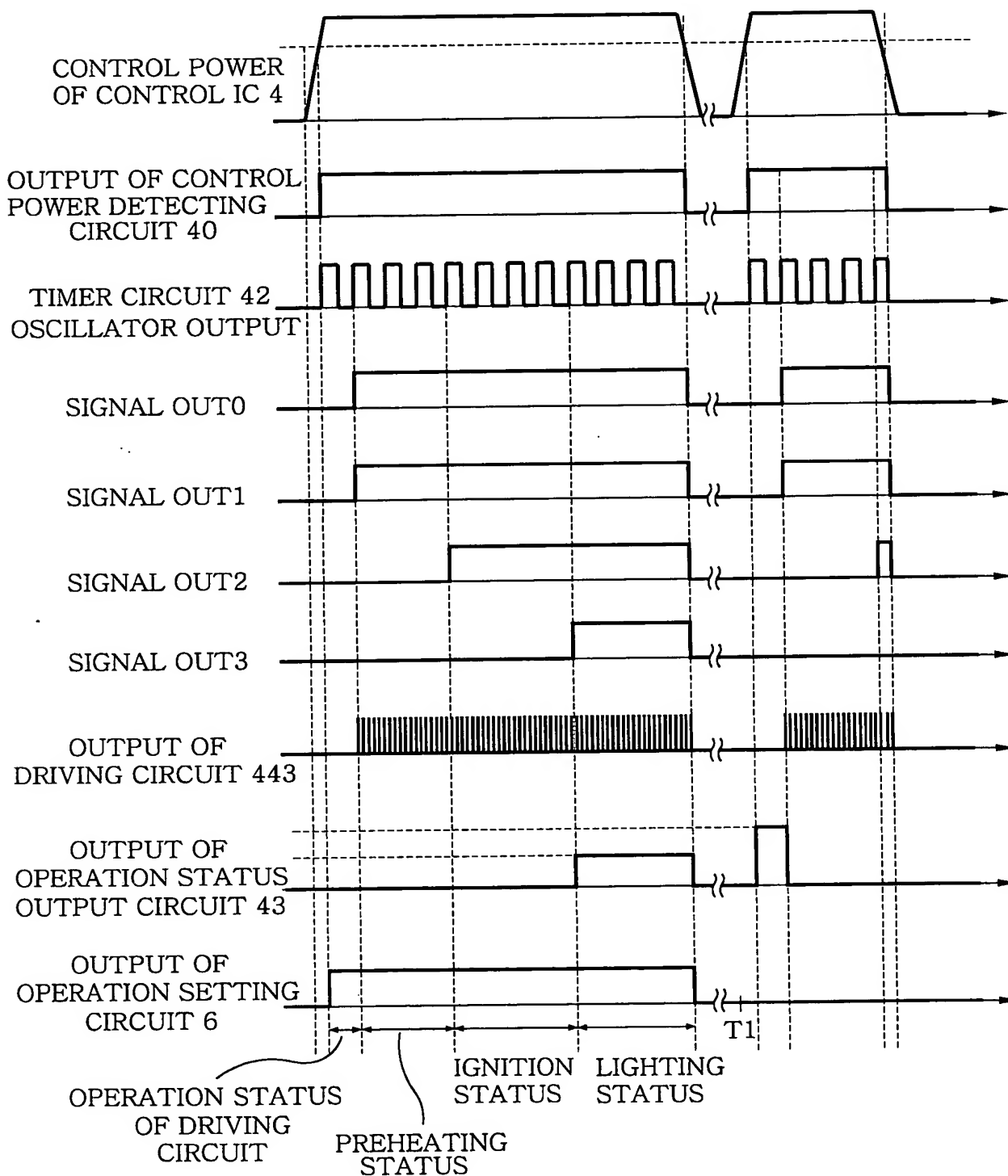


FIG. 17

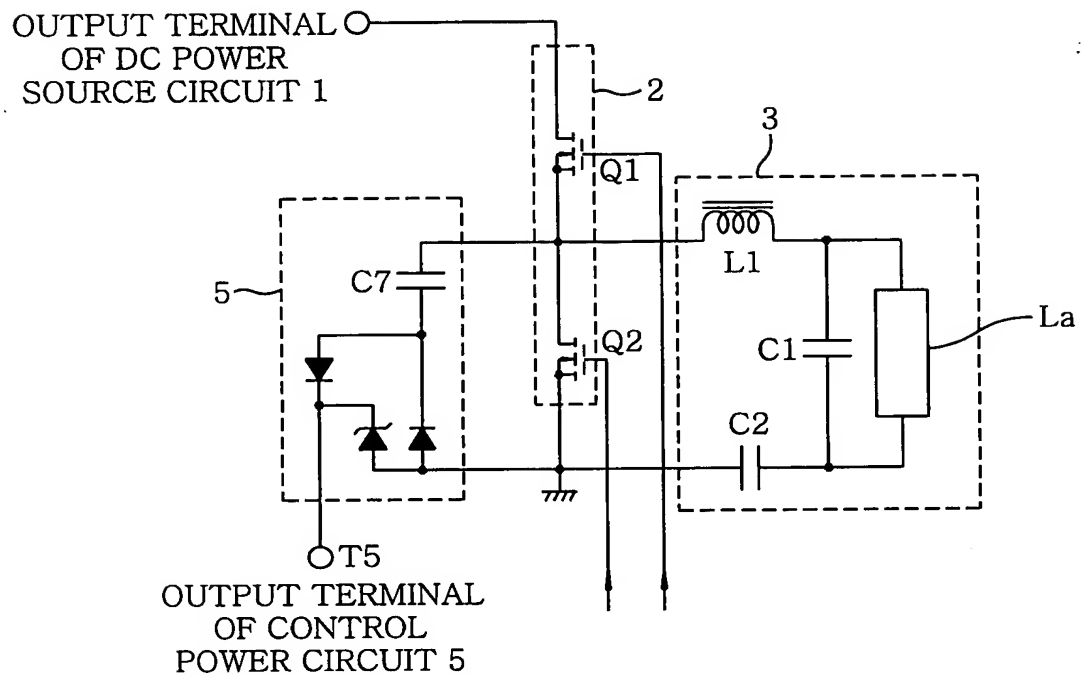


FIG. 18

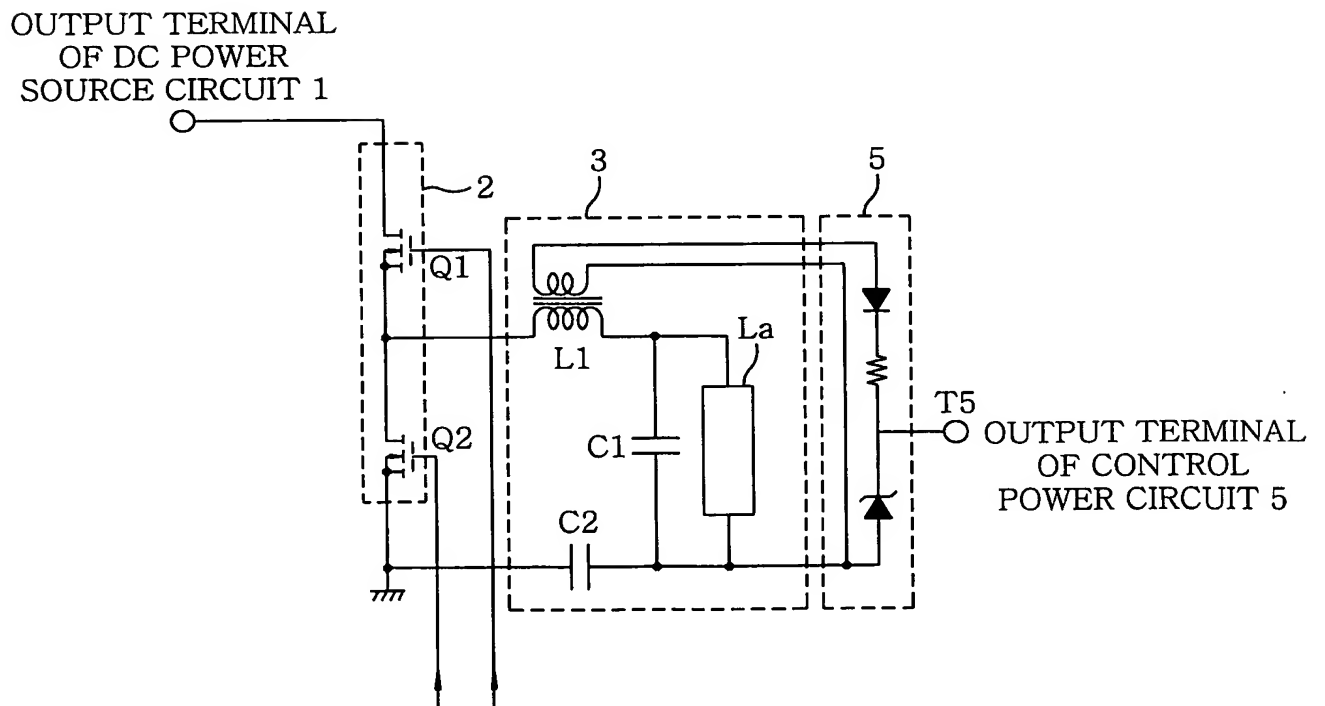


FIG. 19

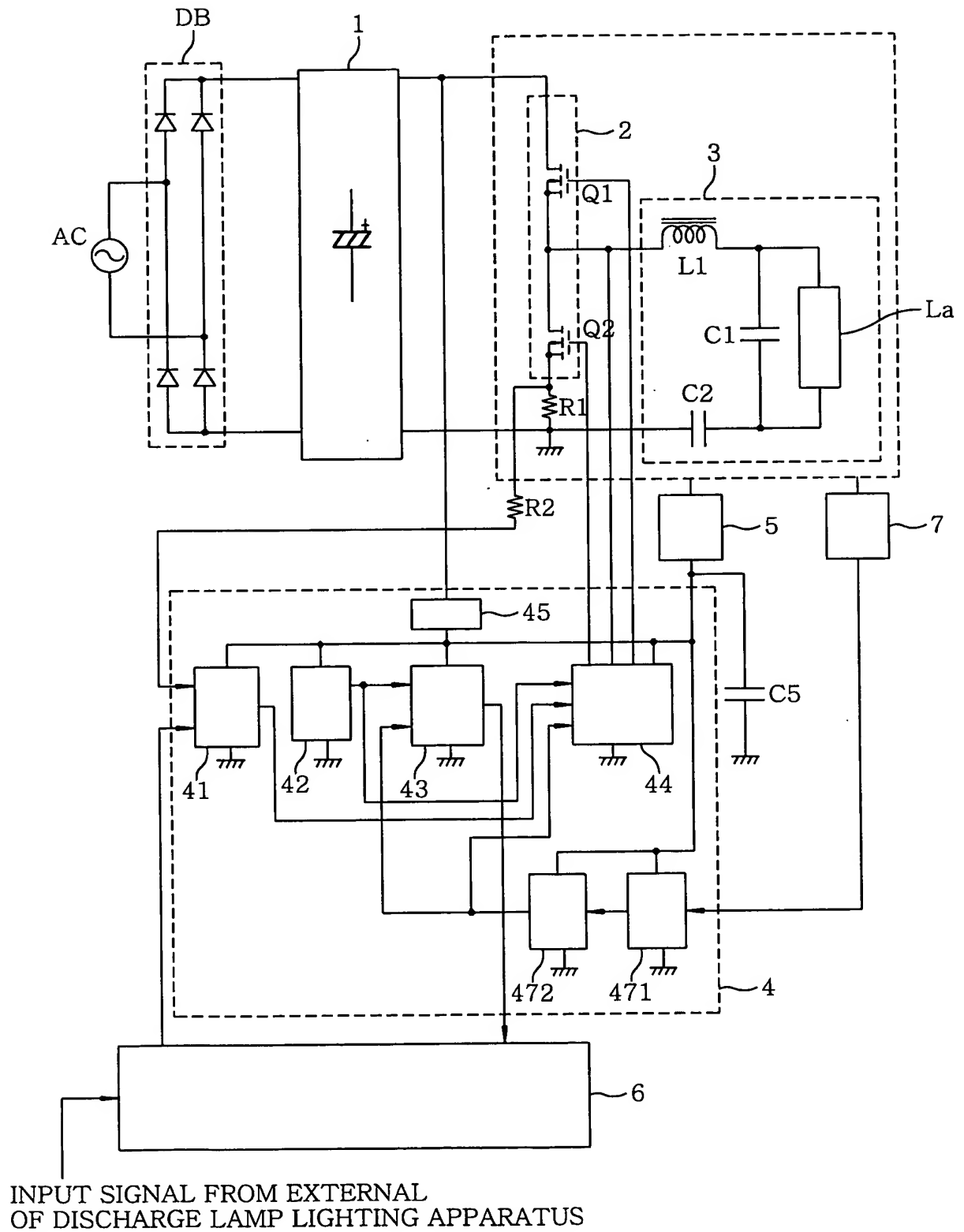


FIG. 20

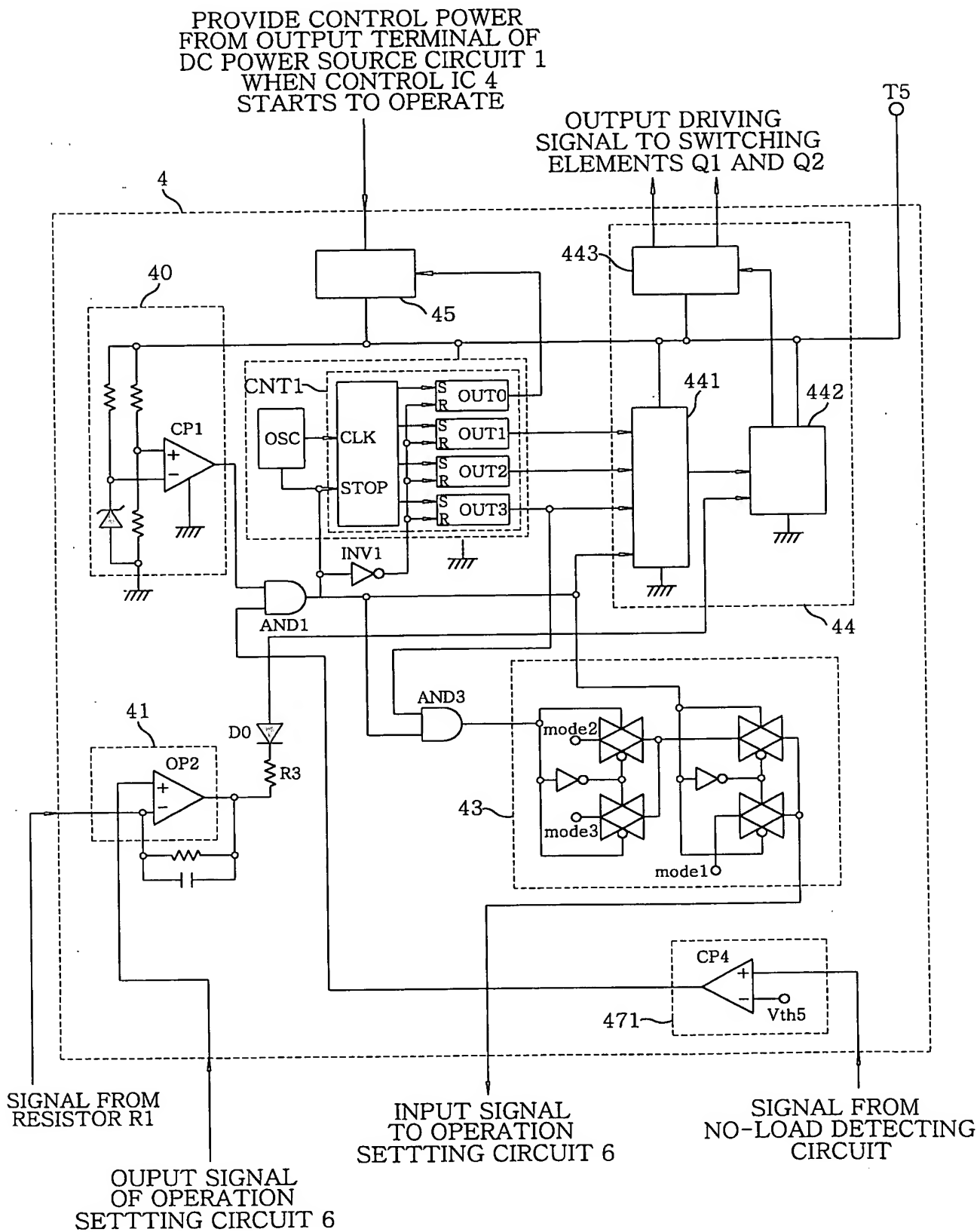


FIG. 21

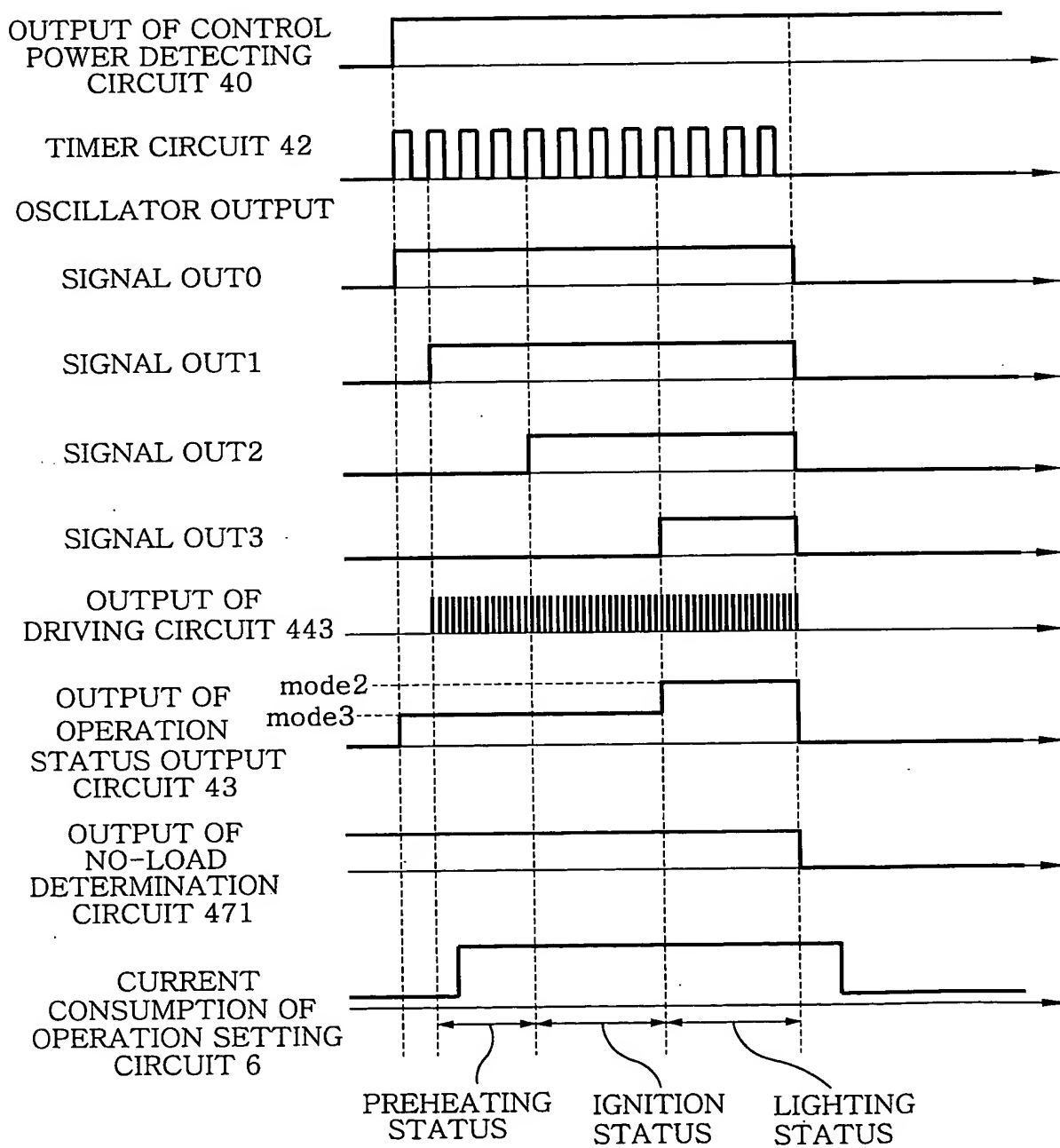


FIG.22

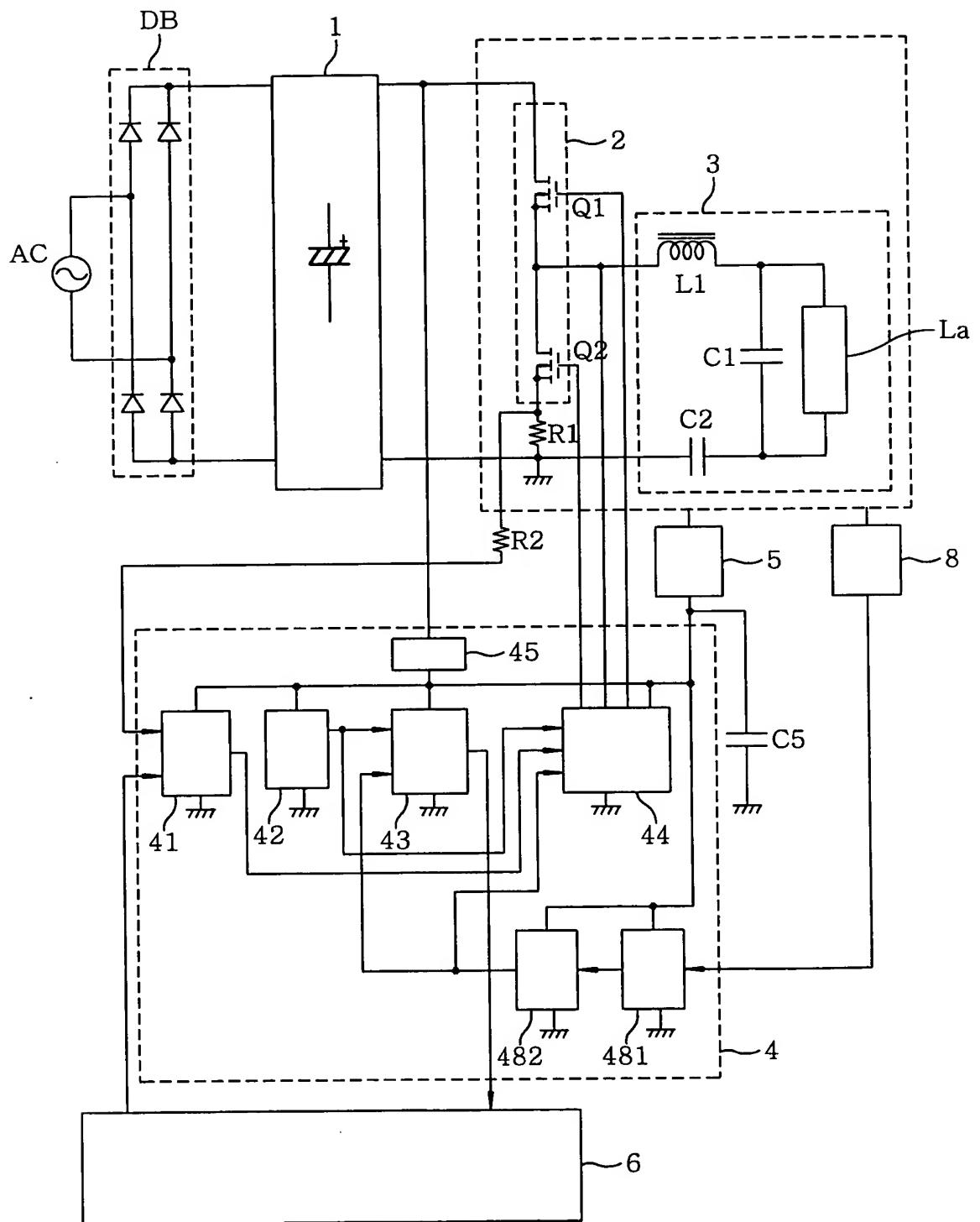


FIG. 23

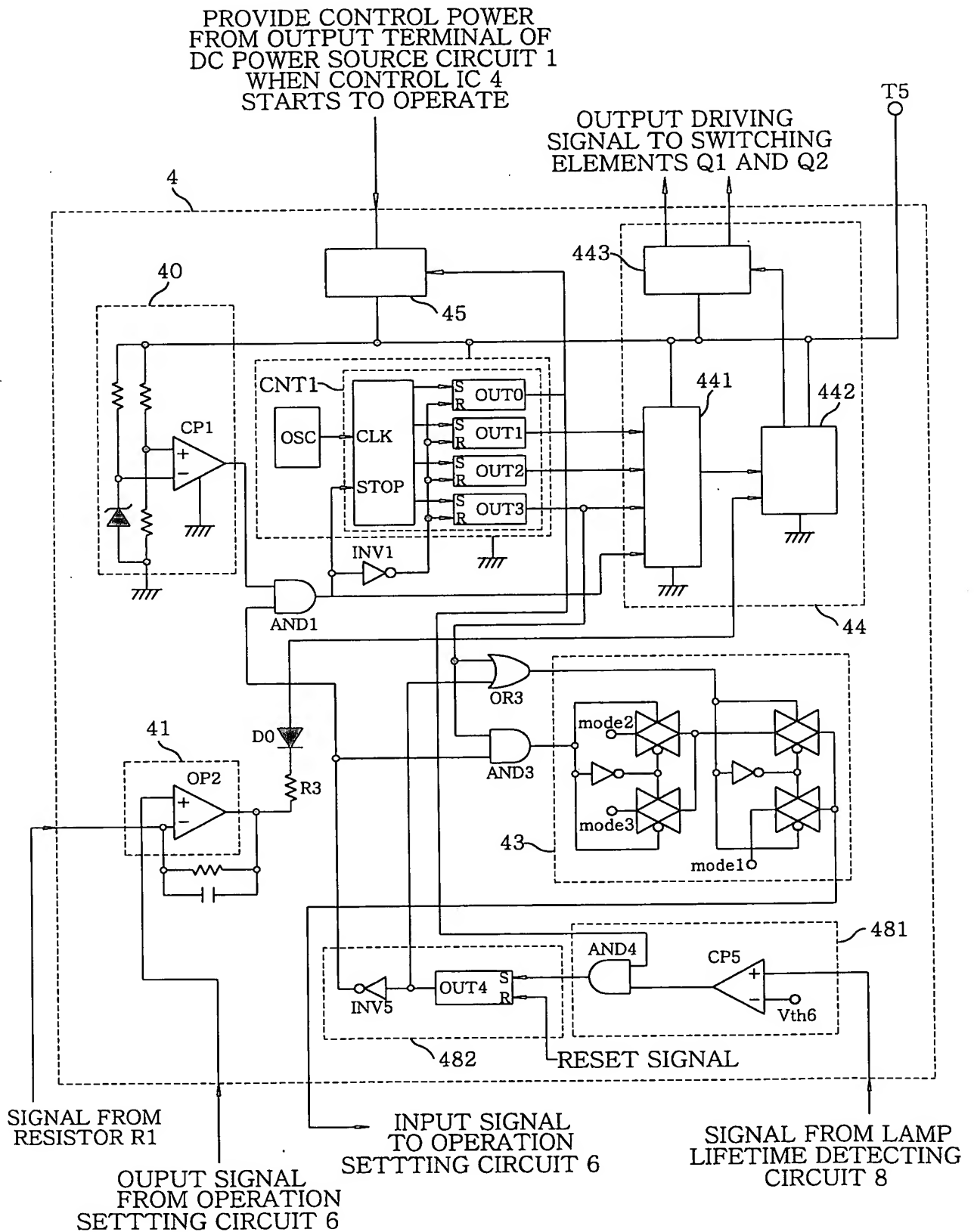


FIG. 24

PROVIDE CONTROL POWER
FROM OUTPUT TERMINAL OF
DC POWER SOURCE CIRCUIT 1
WHEN CONTROL IC 4
STARTS TO OPERATE

OUTPUT DRIVING
SIGNAL TO SWITCHING
ELEMENTS Q1 AND Q2

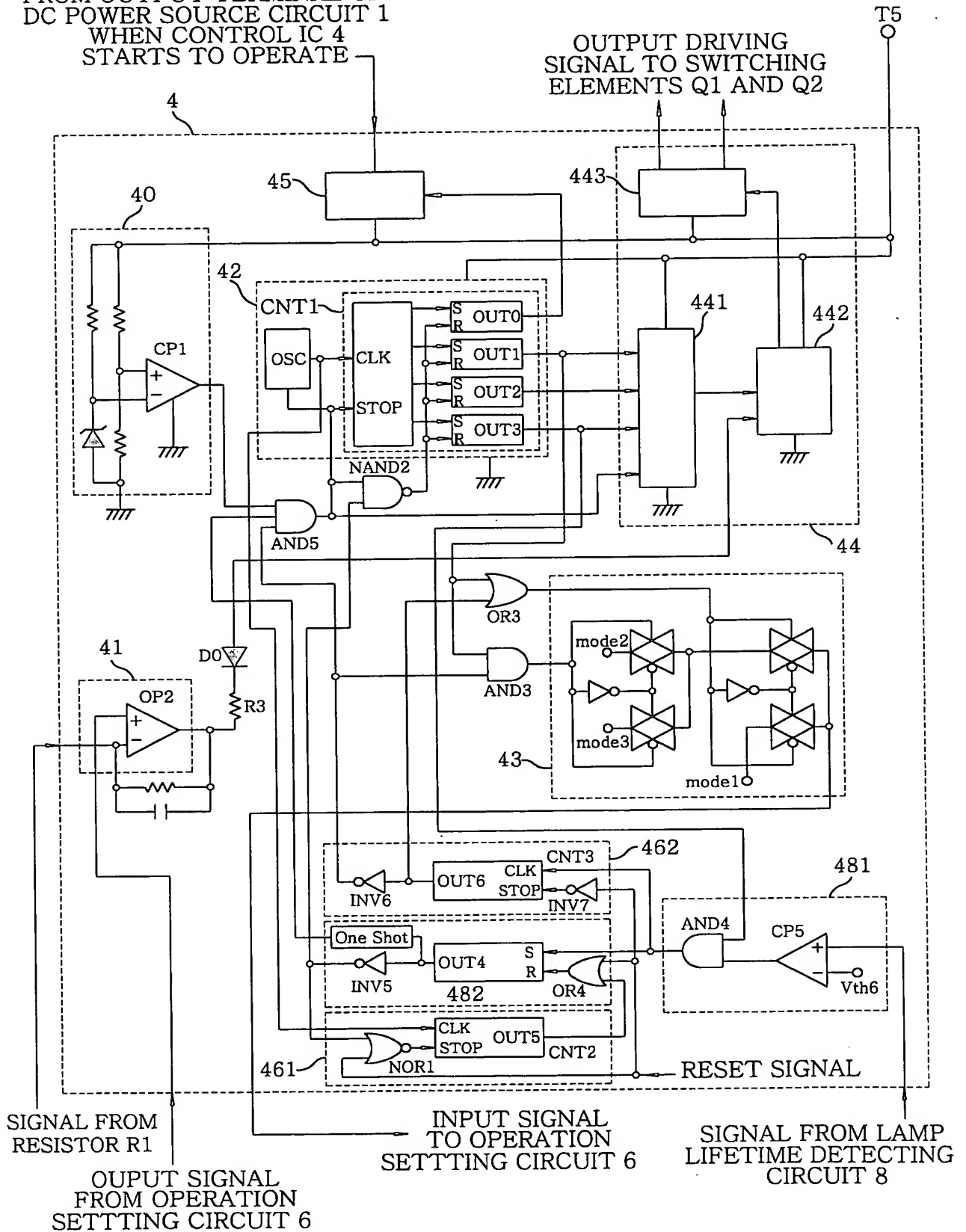


FIG.25

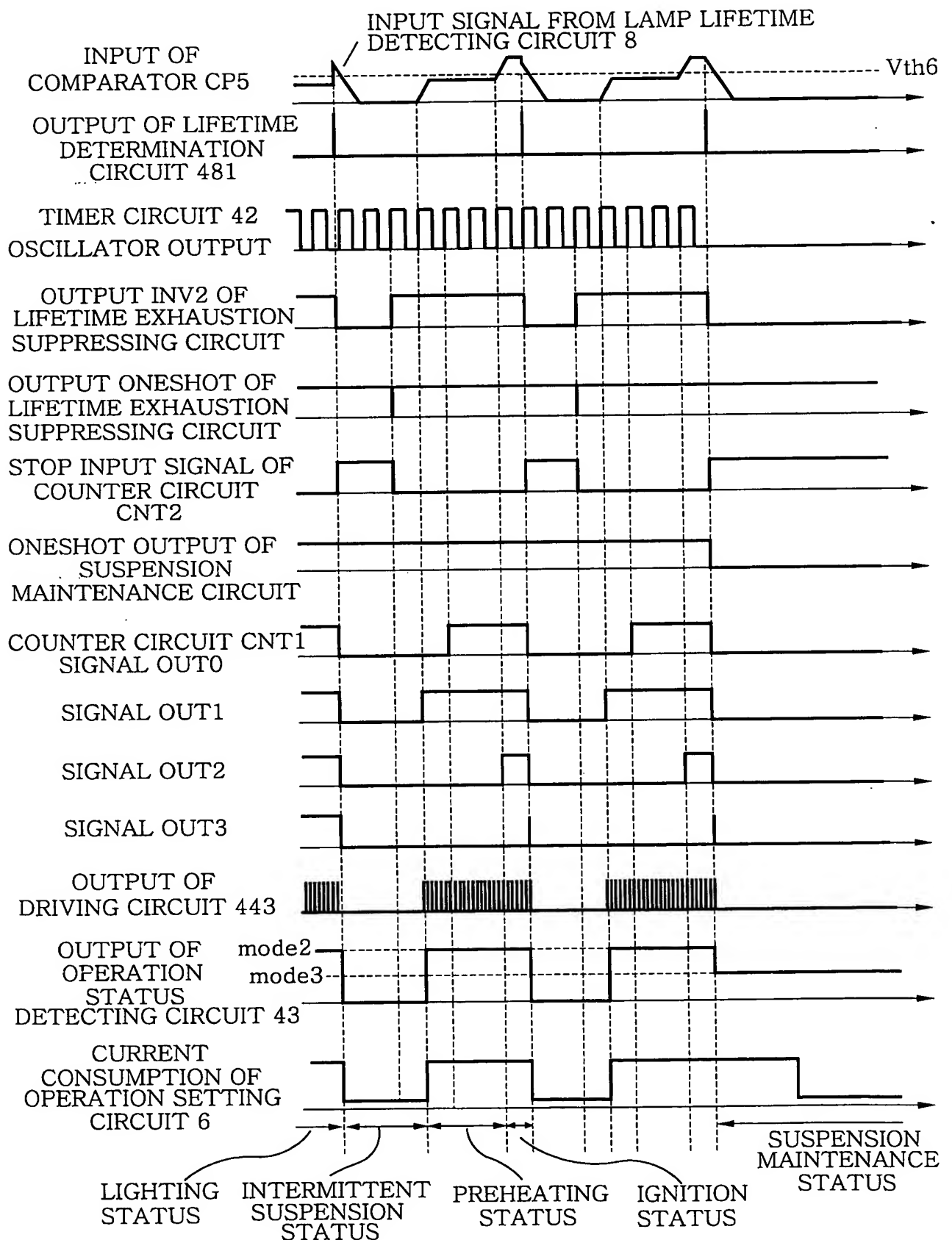


FIG. 26

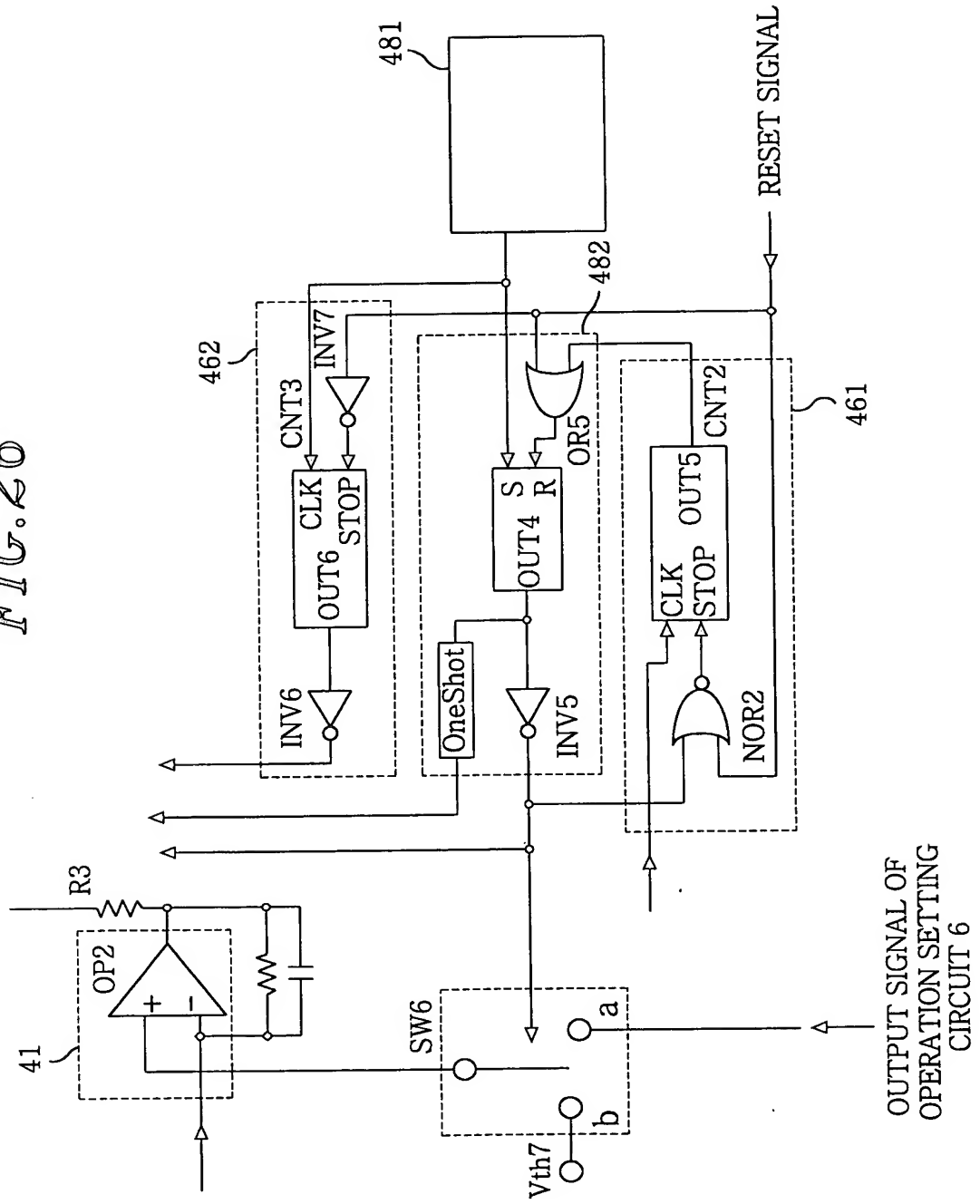


FIG.27

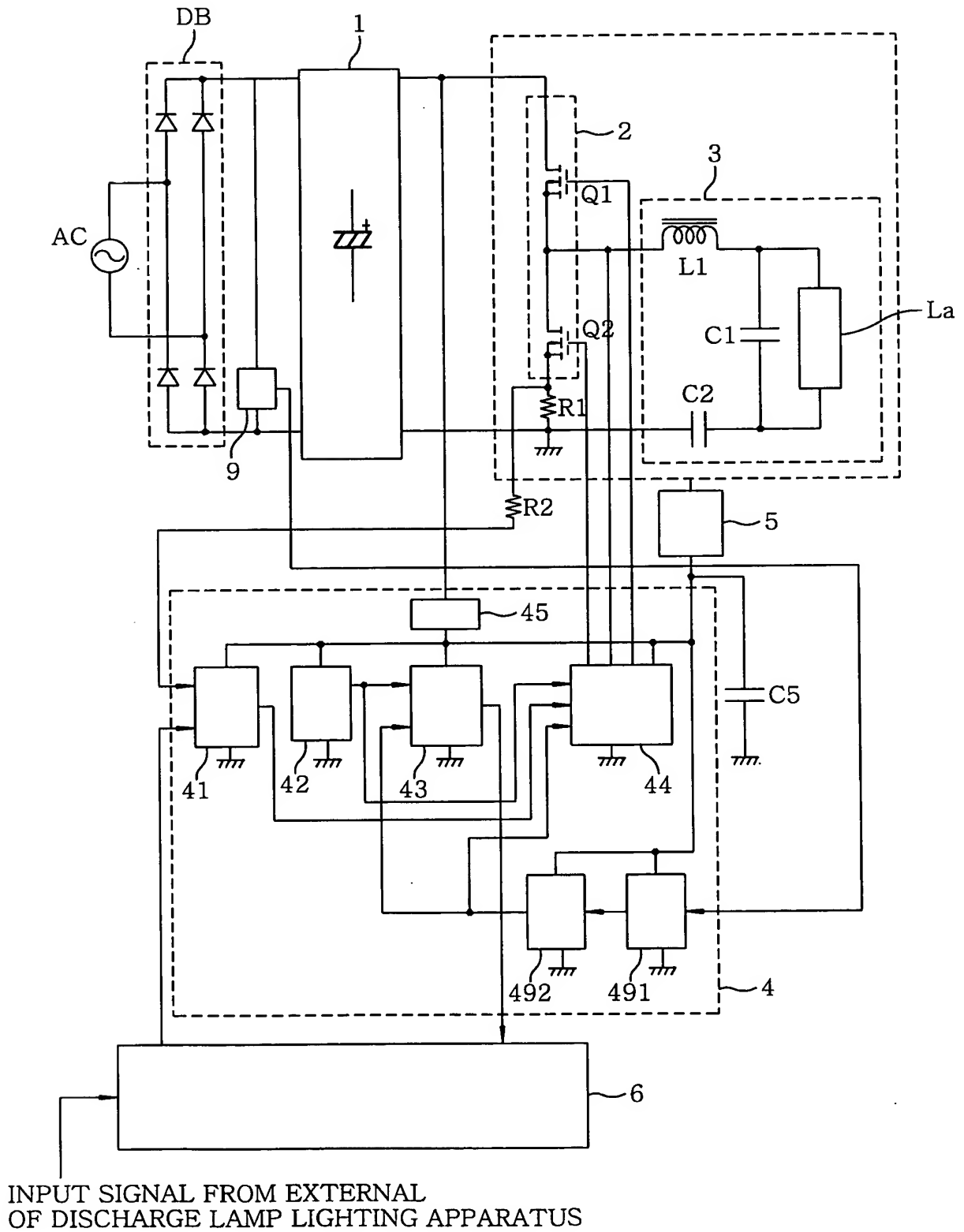


FIG. 28

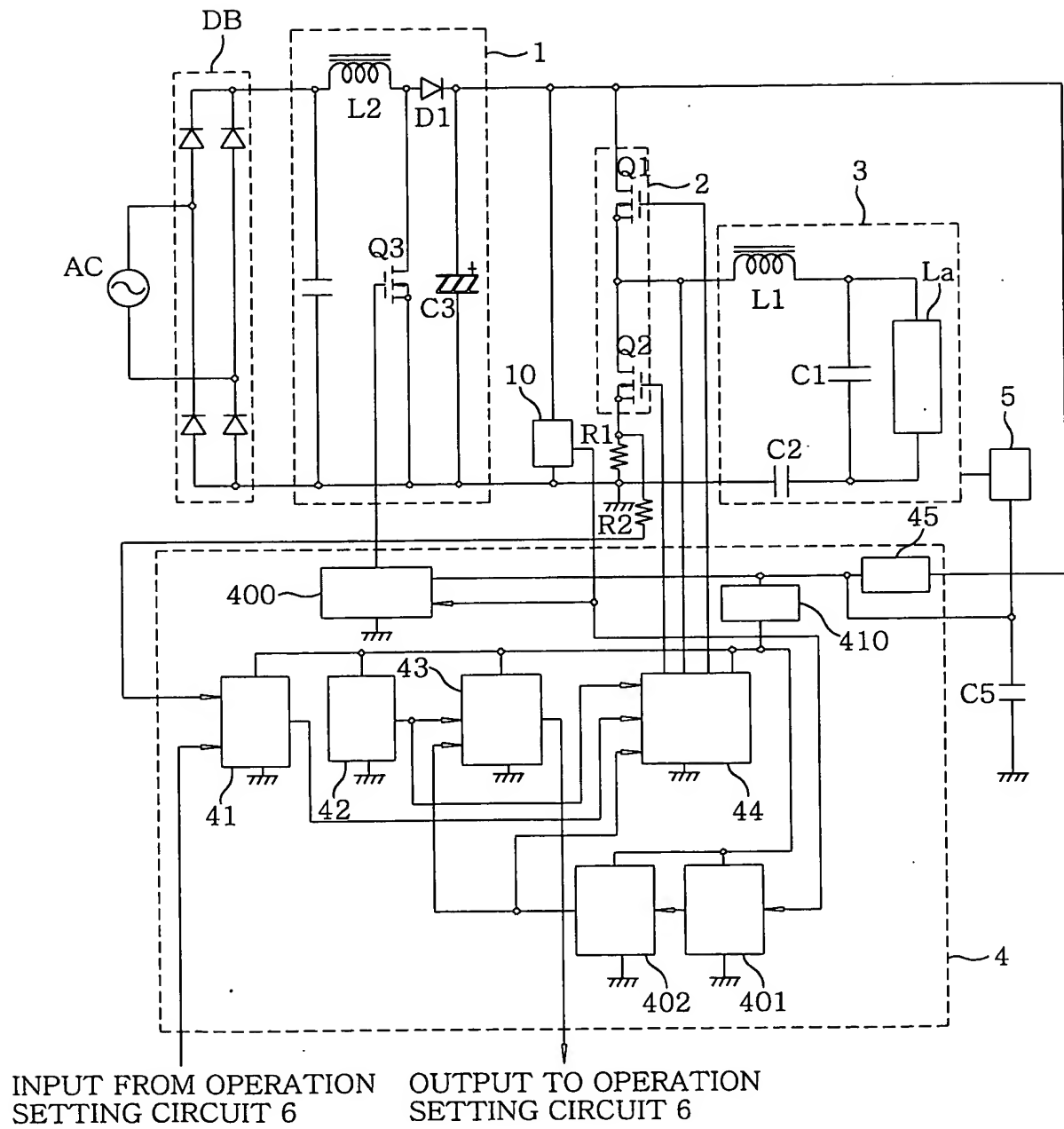


FIG. 29

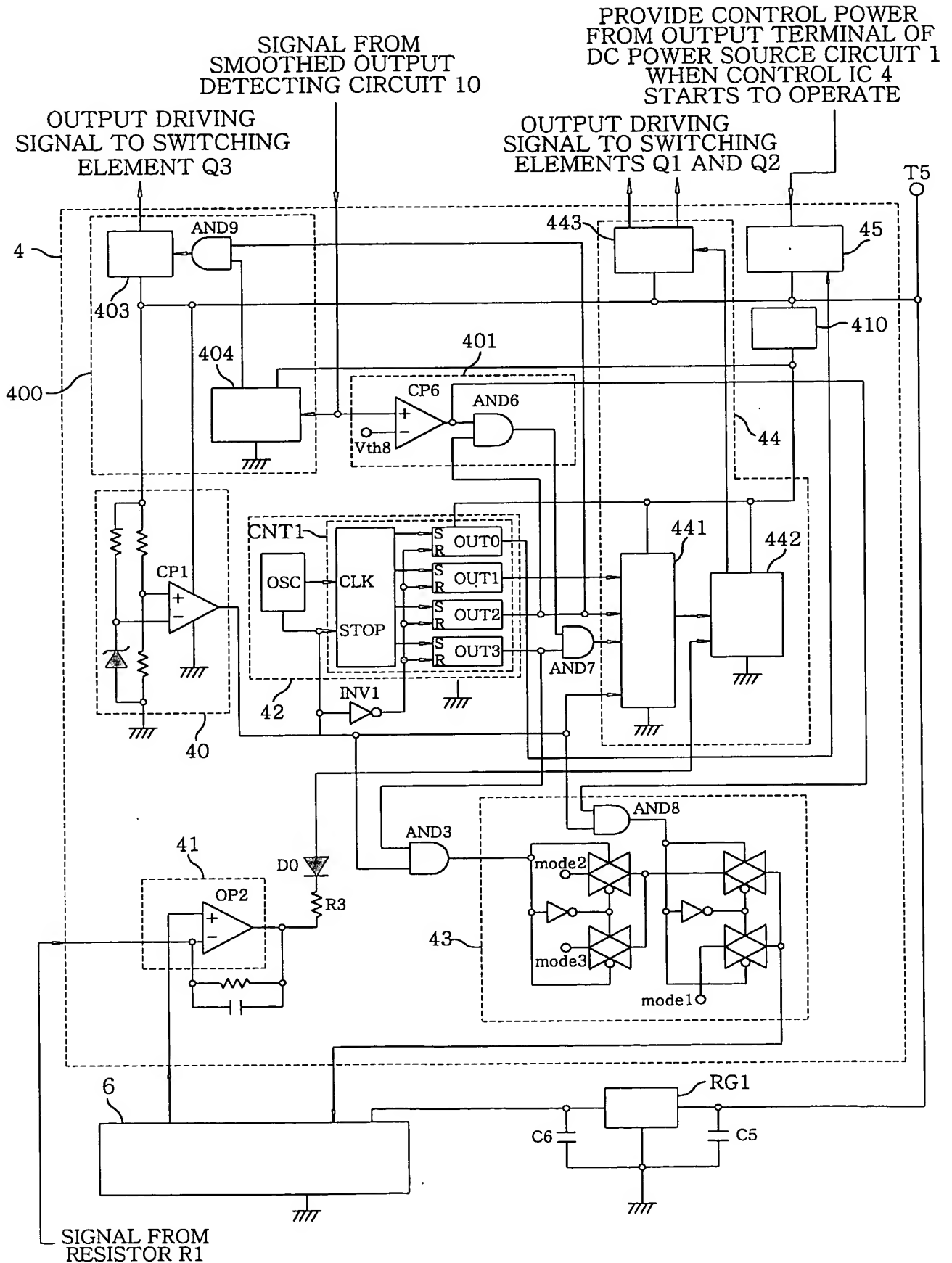


FIG.30

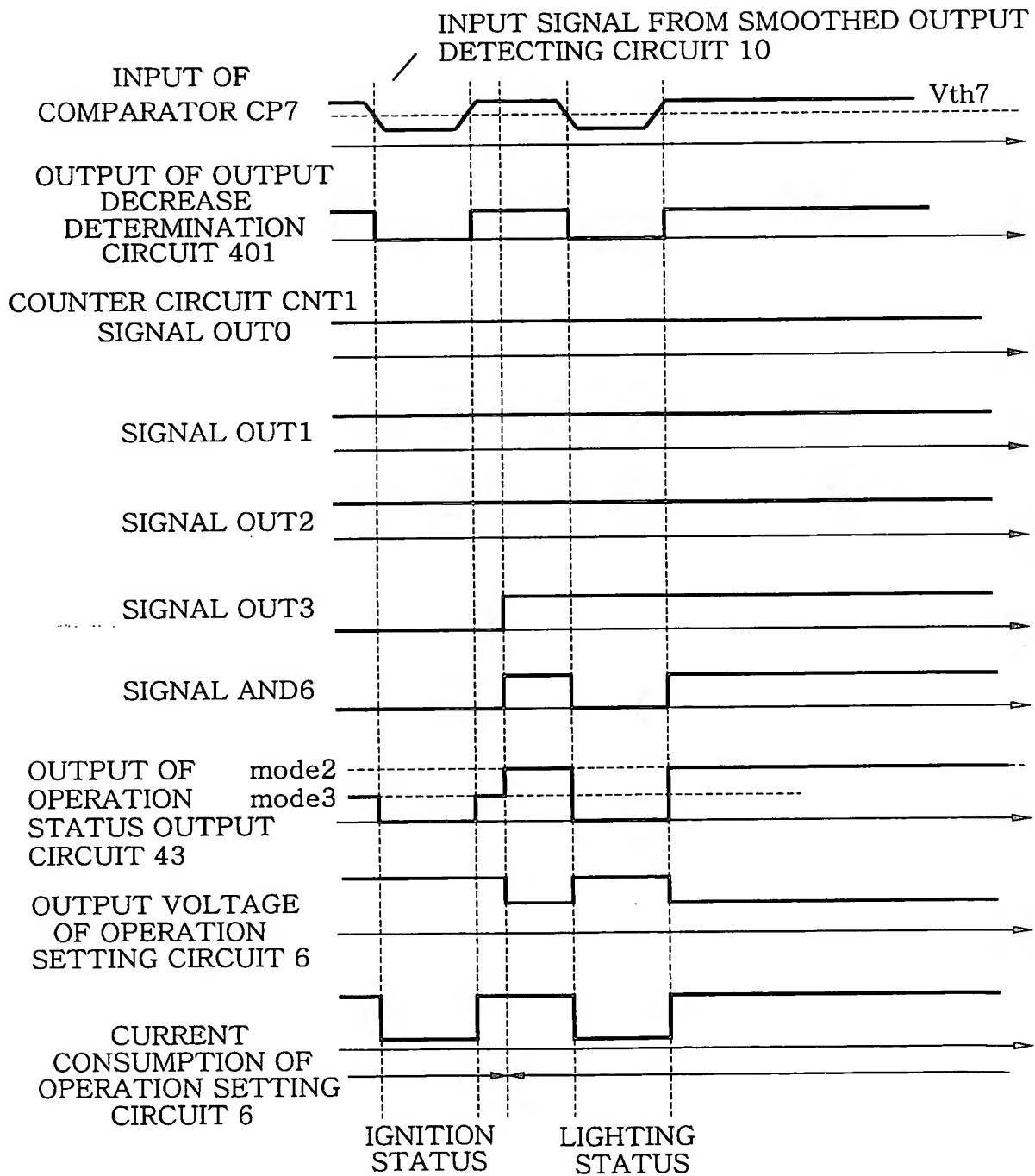


FIG. 31

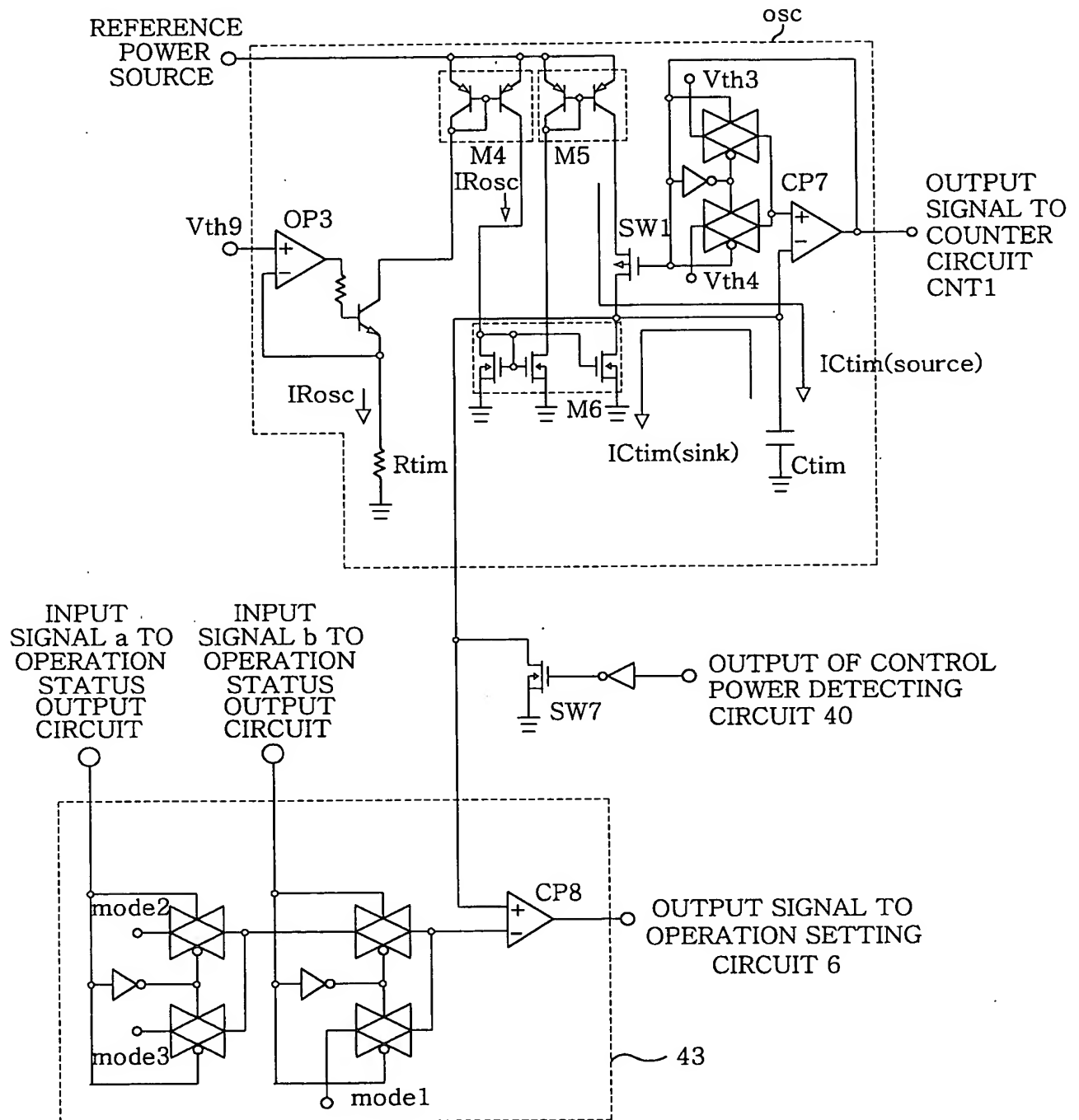


FIG. 32

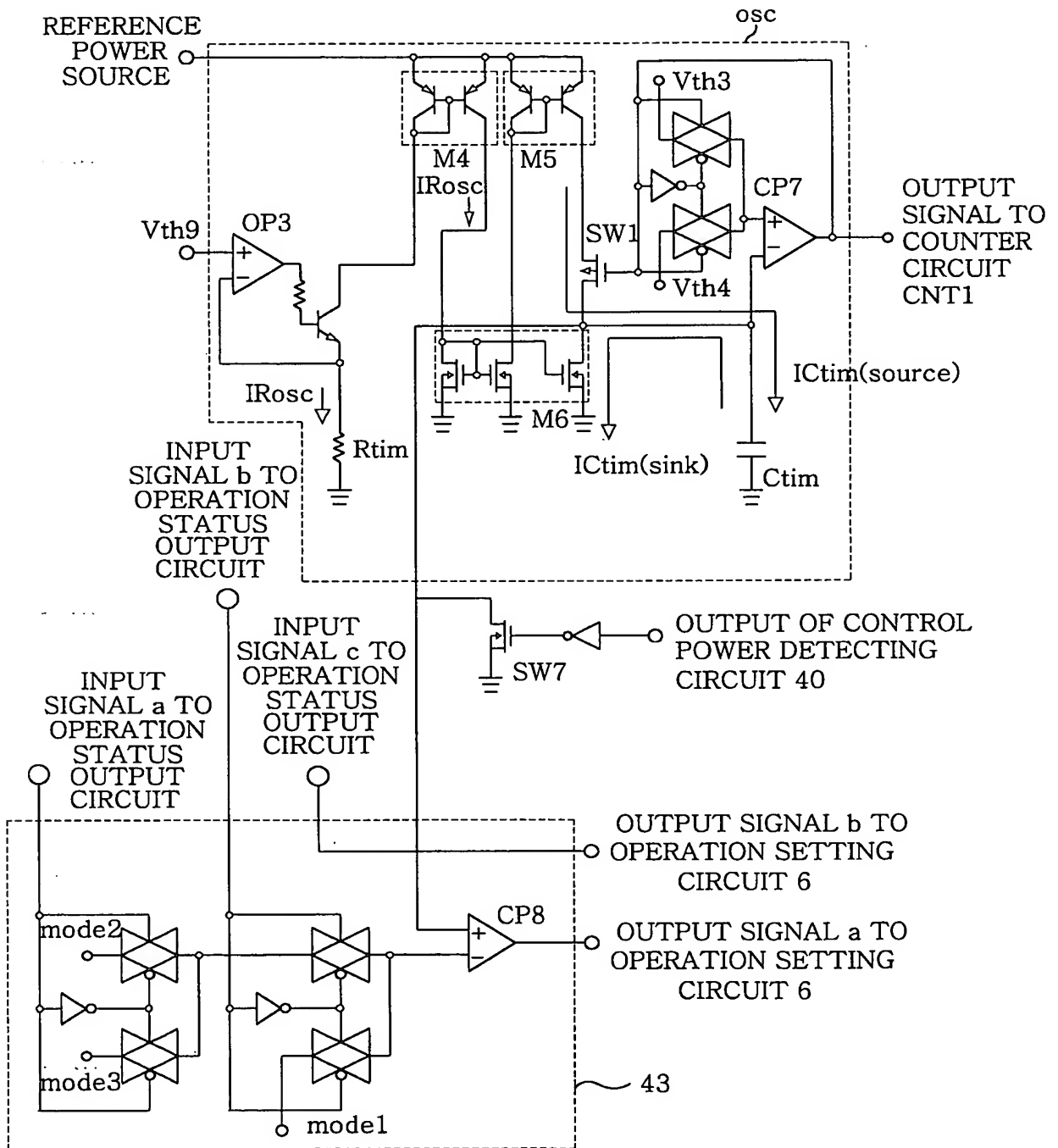


FIG. 33

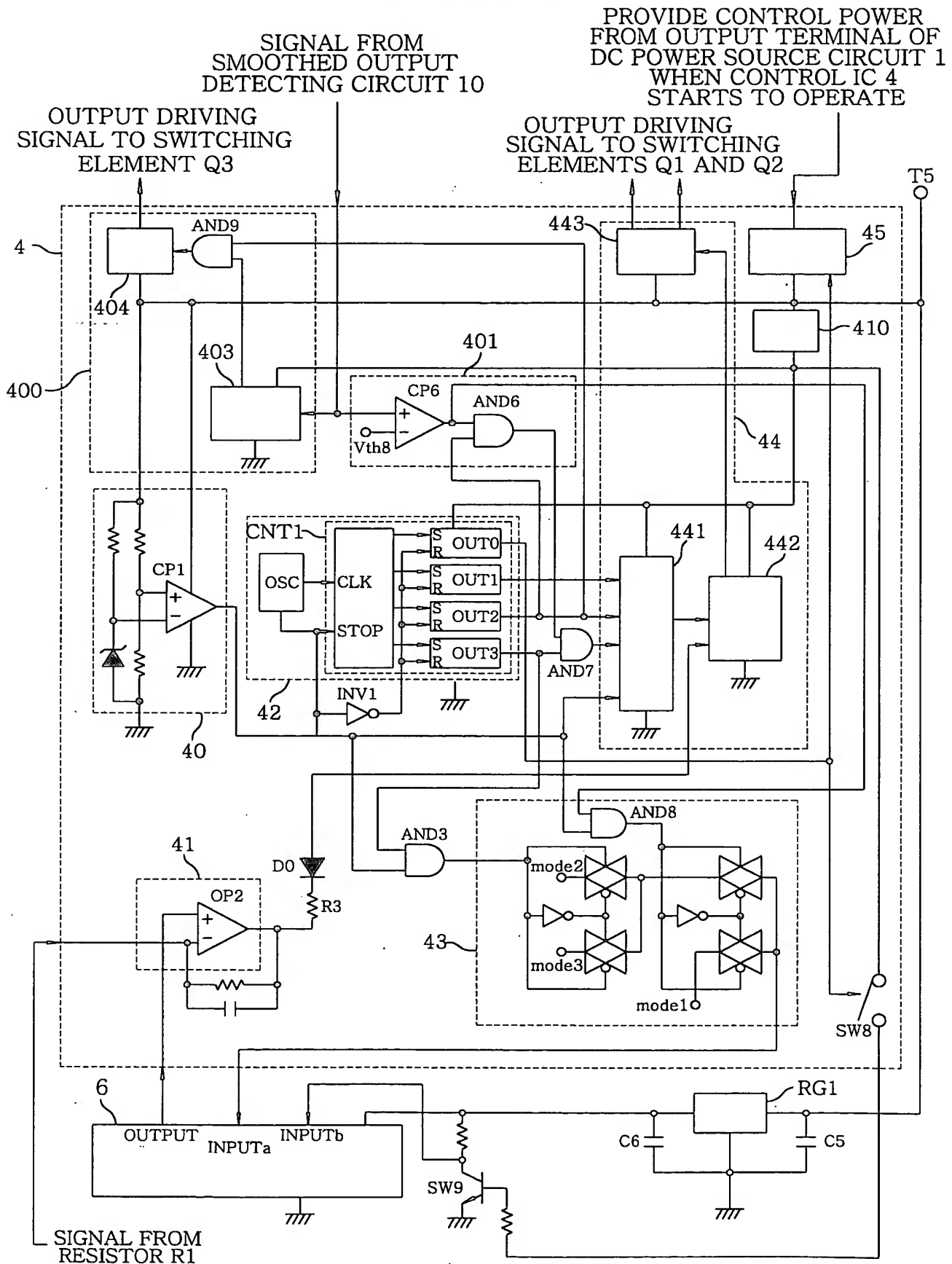


FIG. 34

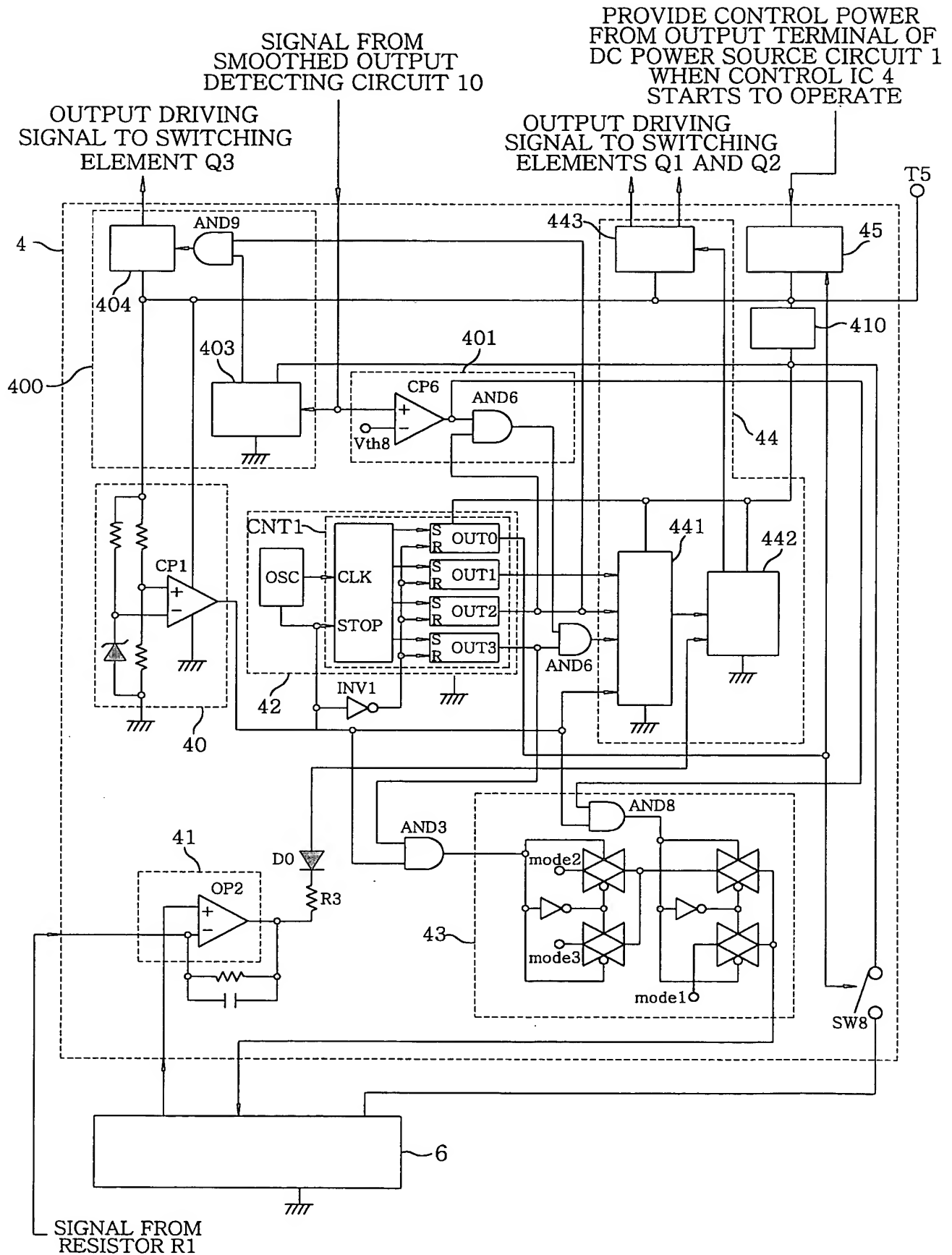


FIG. 35

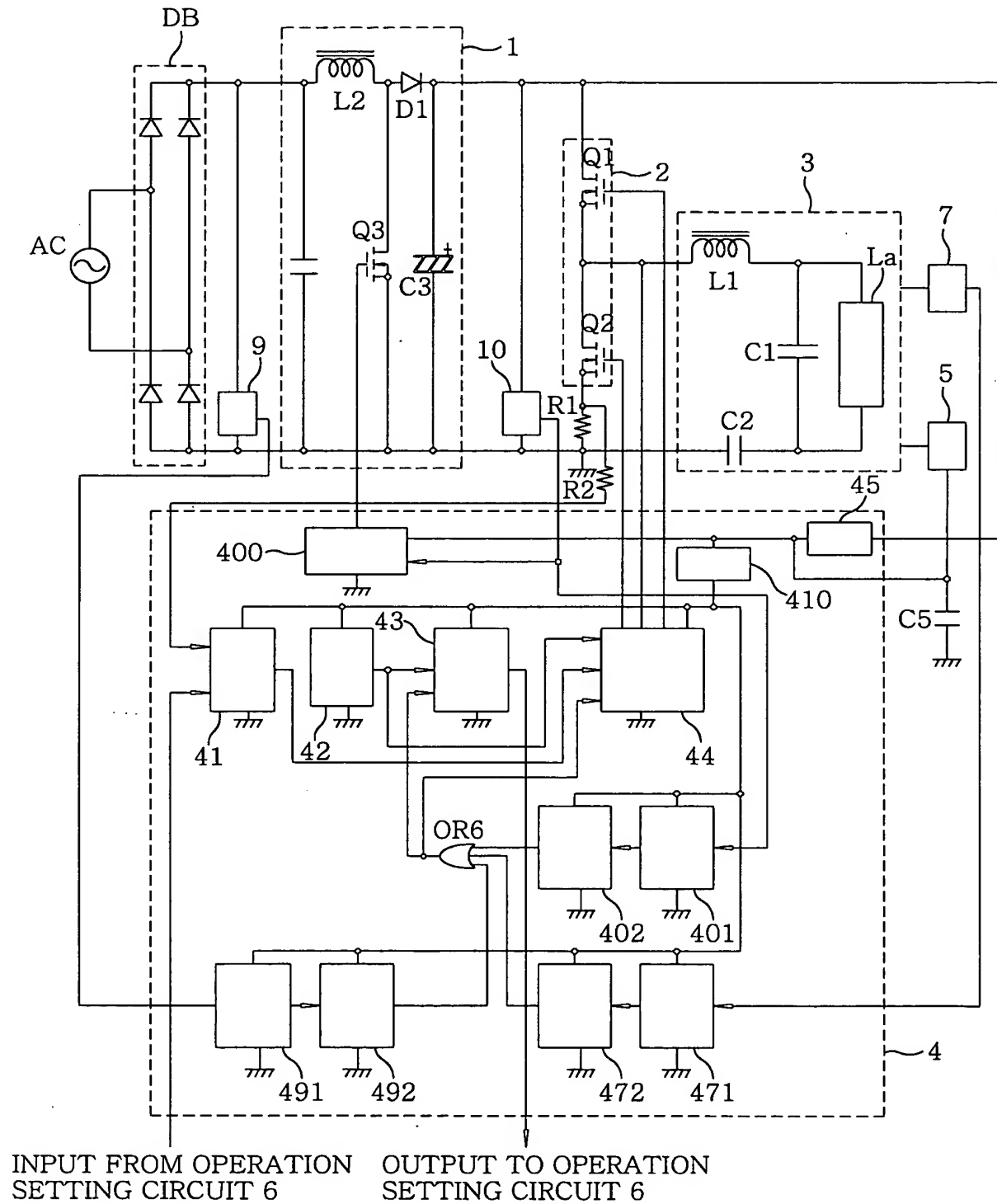


FIG.36

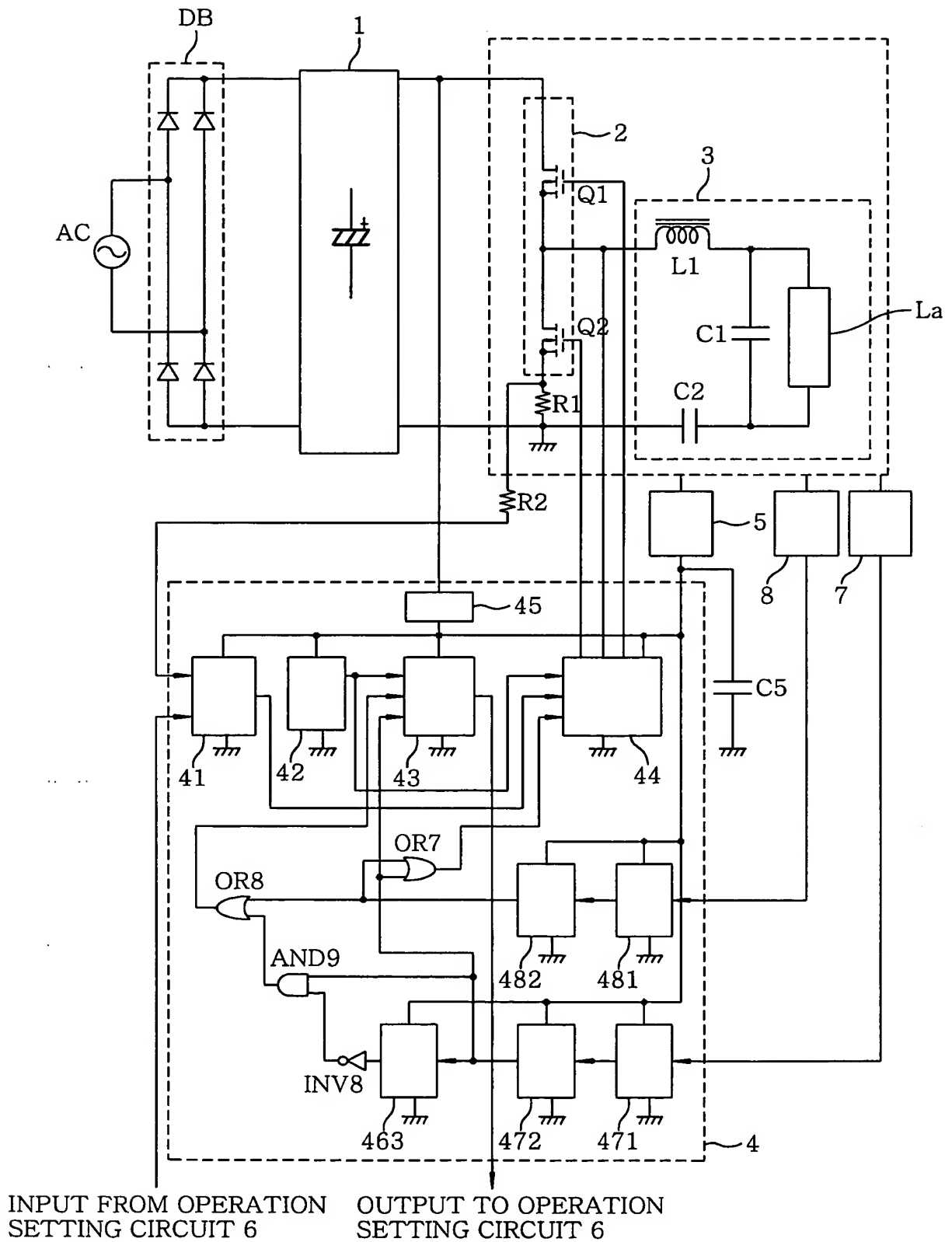


FIG.37

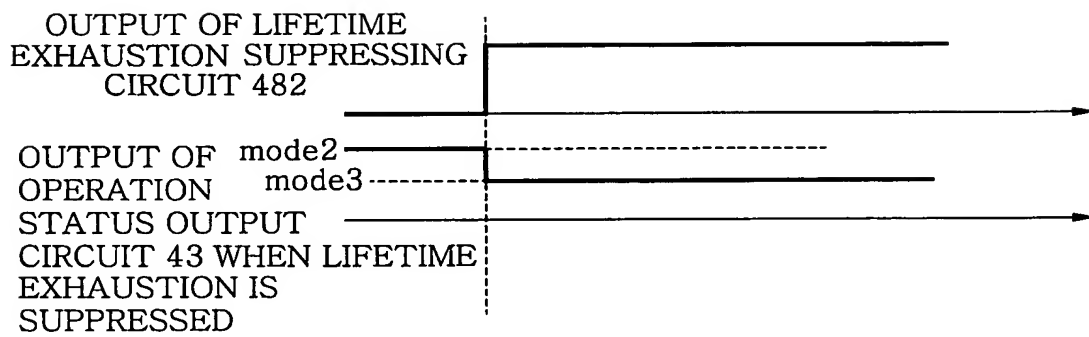


FIG.38

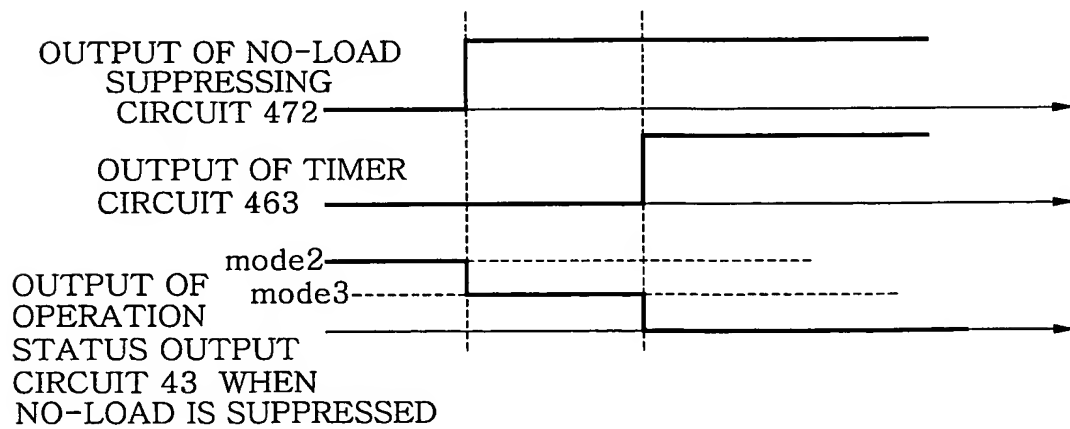


FIG. 39

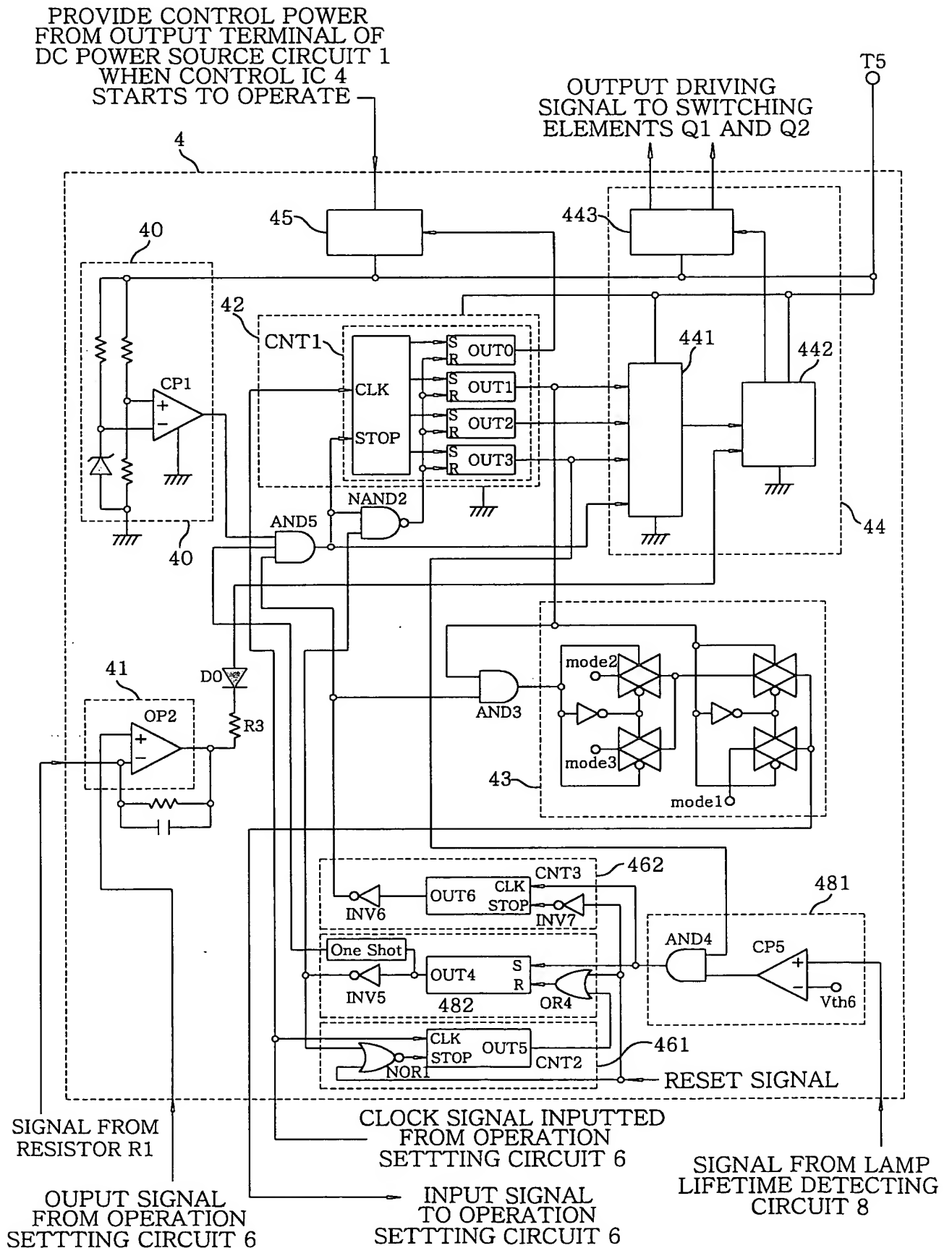


FIG. 40

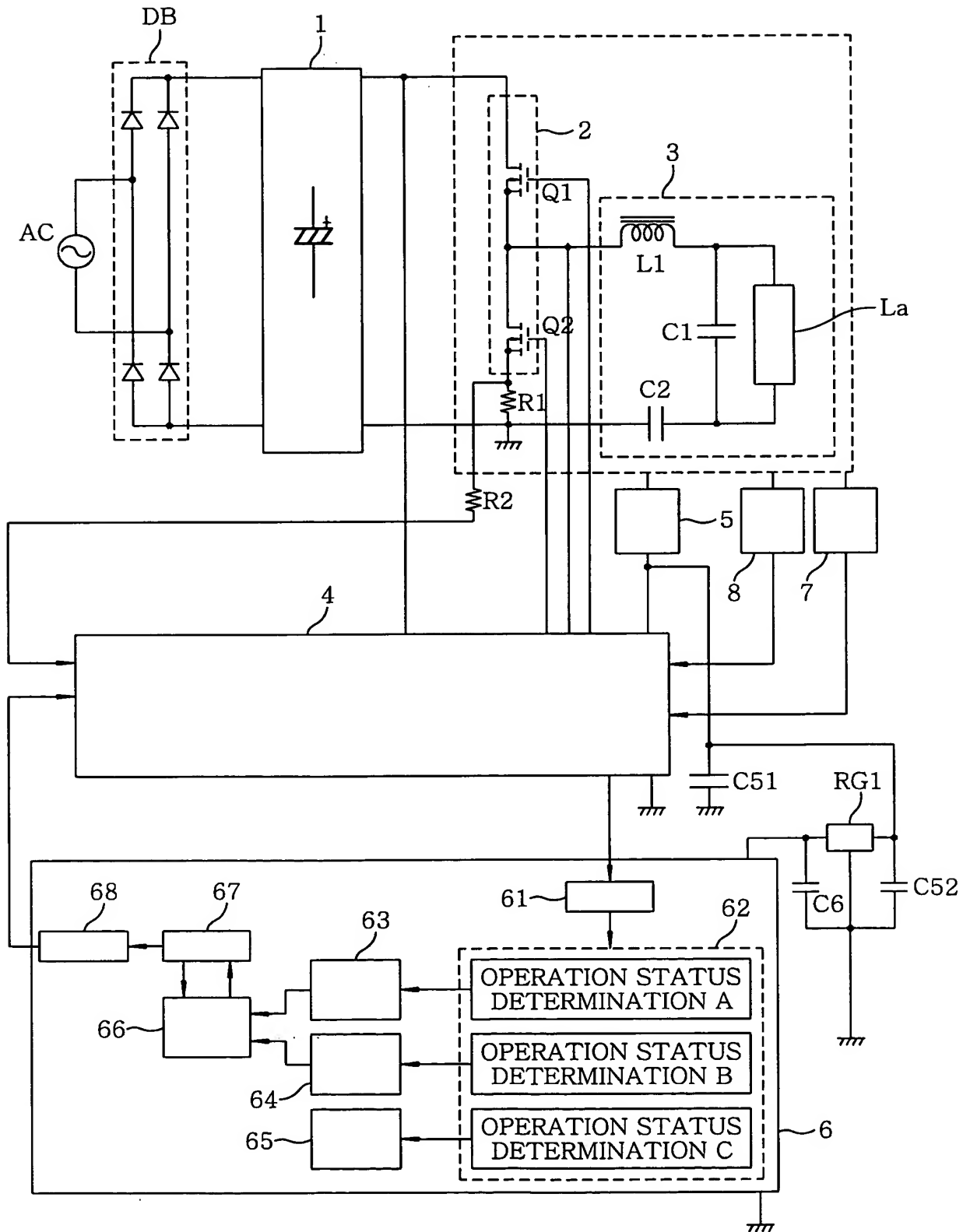


FIG. 41

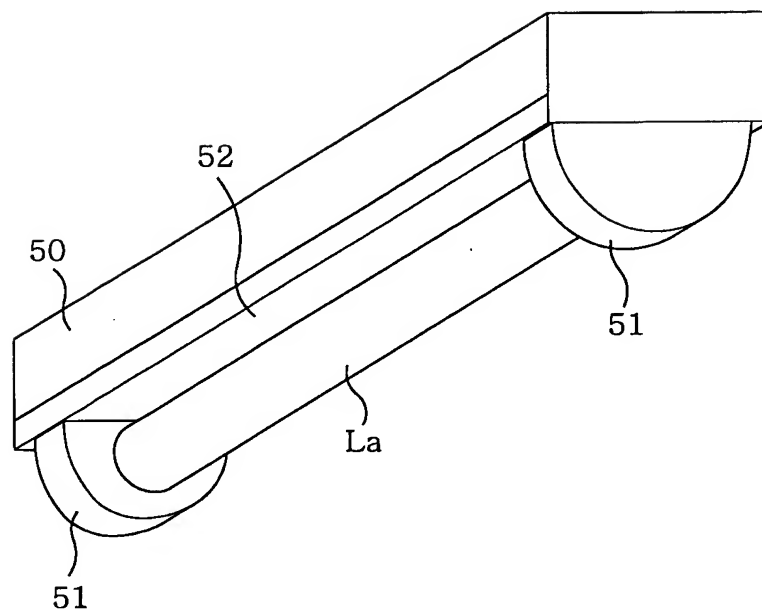


FIG. 42
(PRIOR ART)

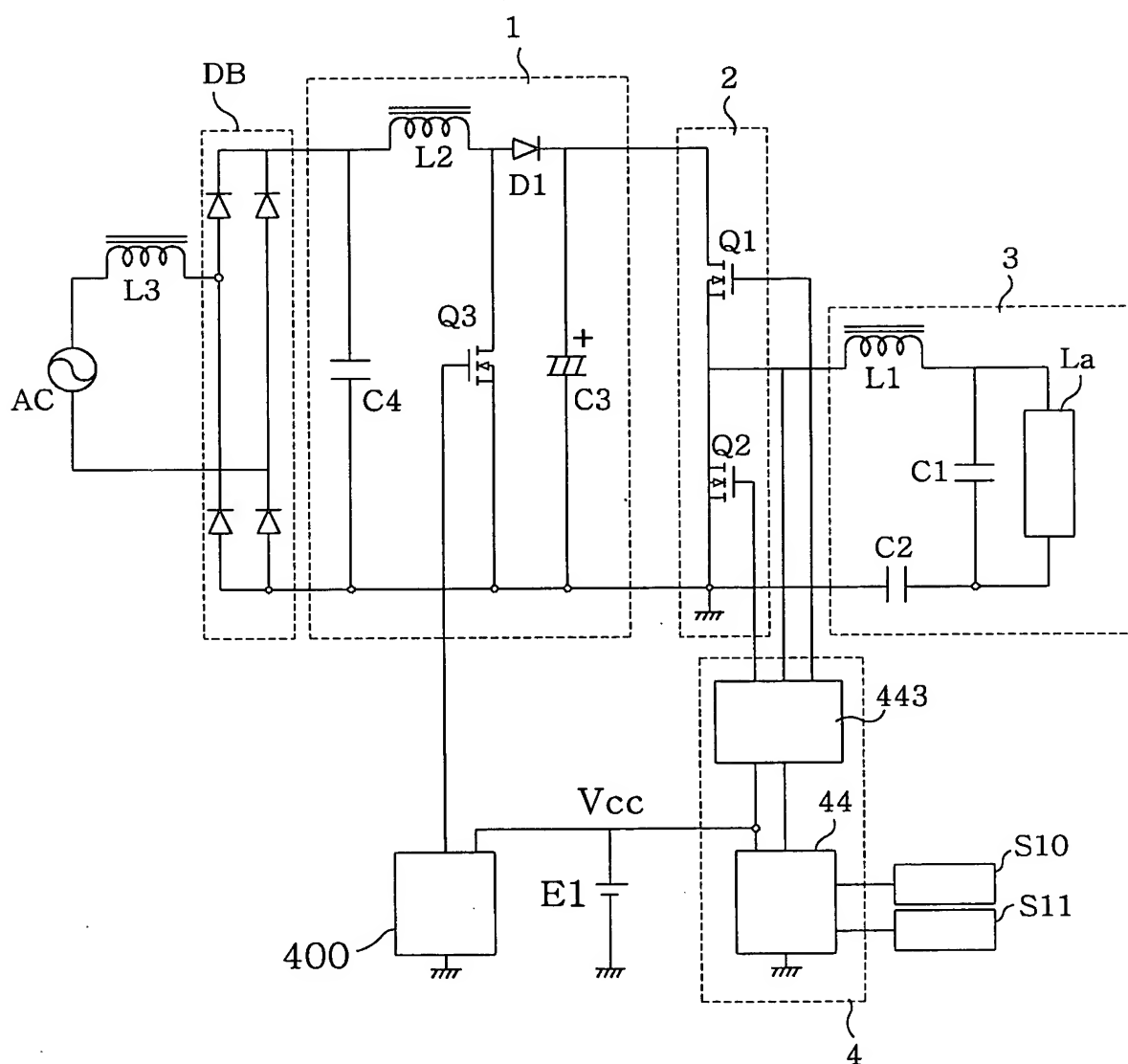


FIG. 43
(PRIOR ART)

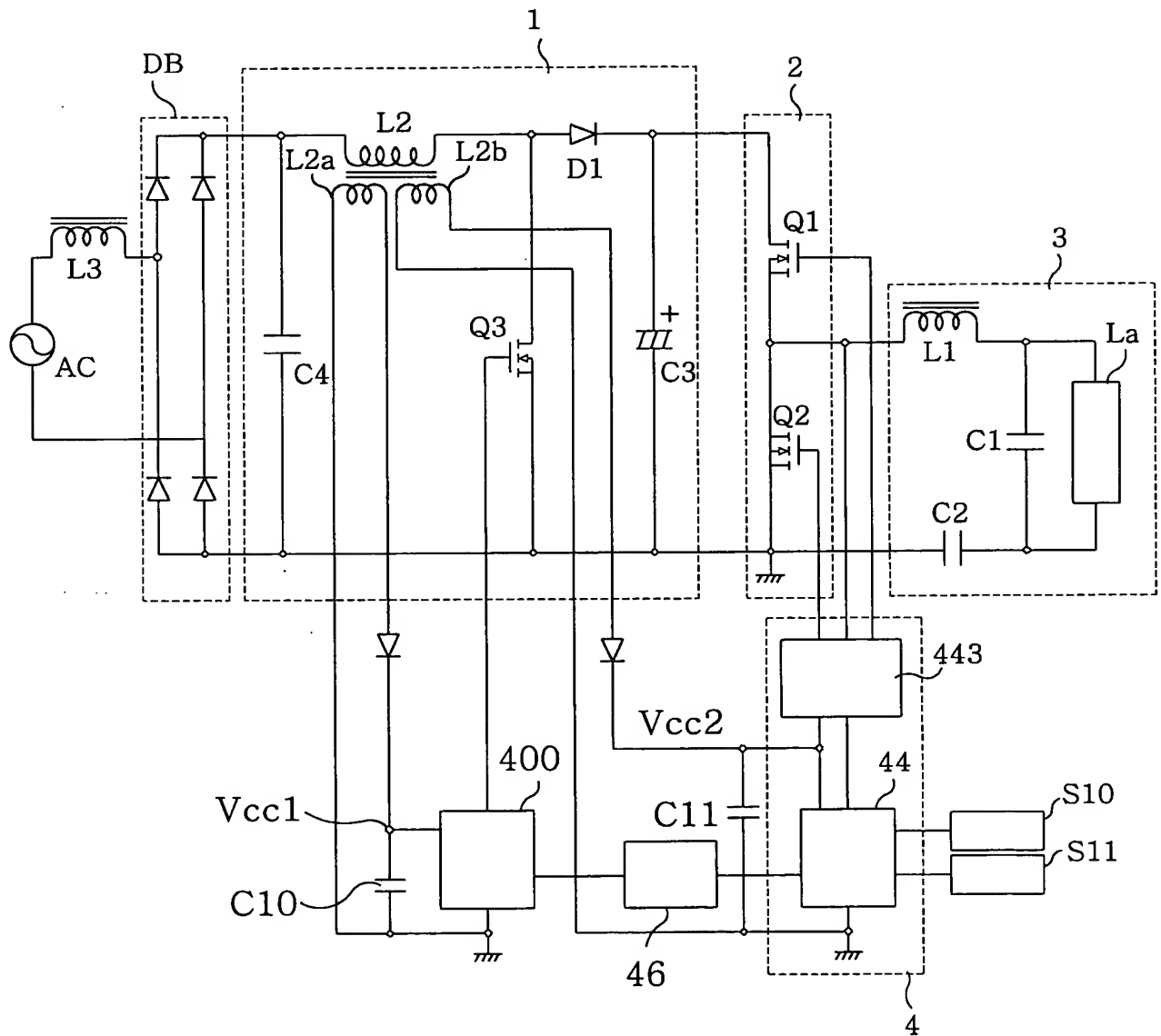


FIG. 44
(PRIOR ART)

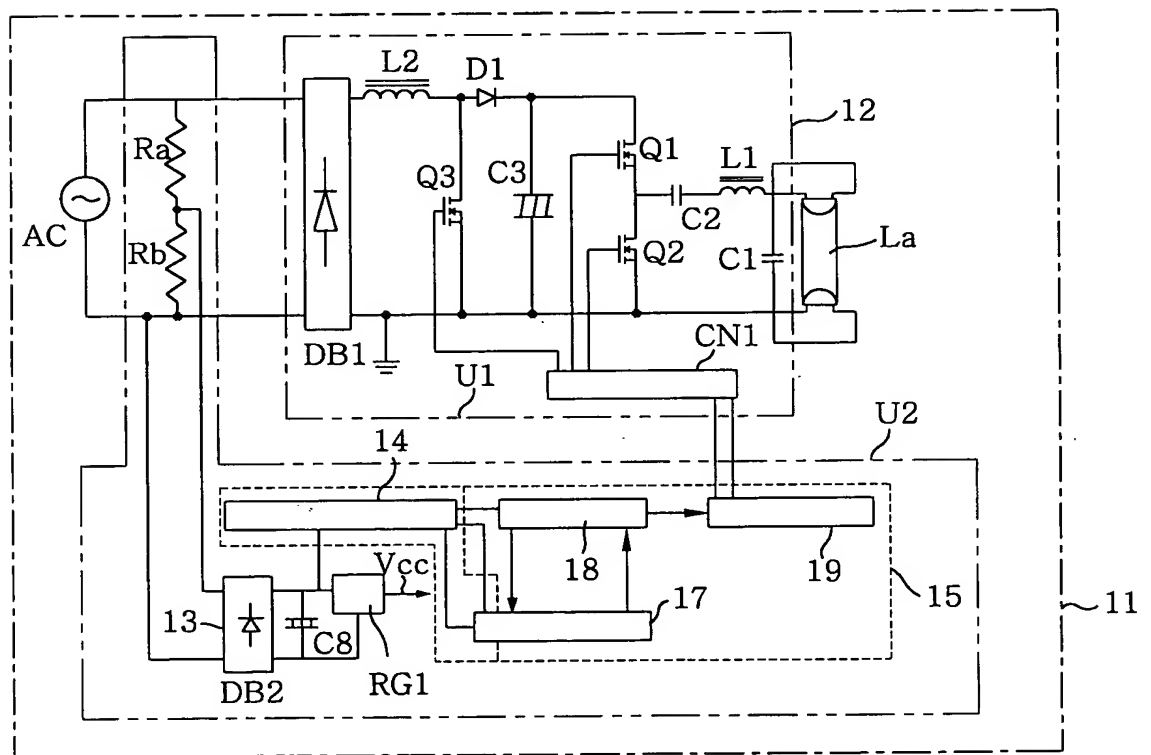


FIG. 45
(PRIOR ART)

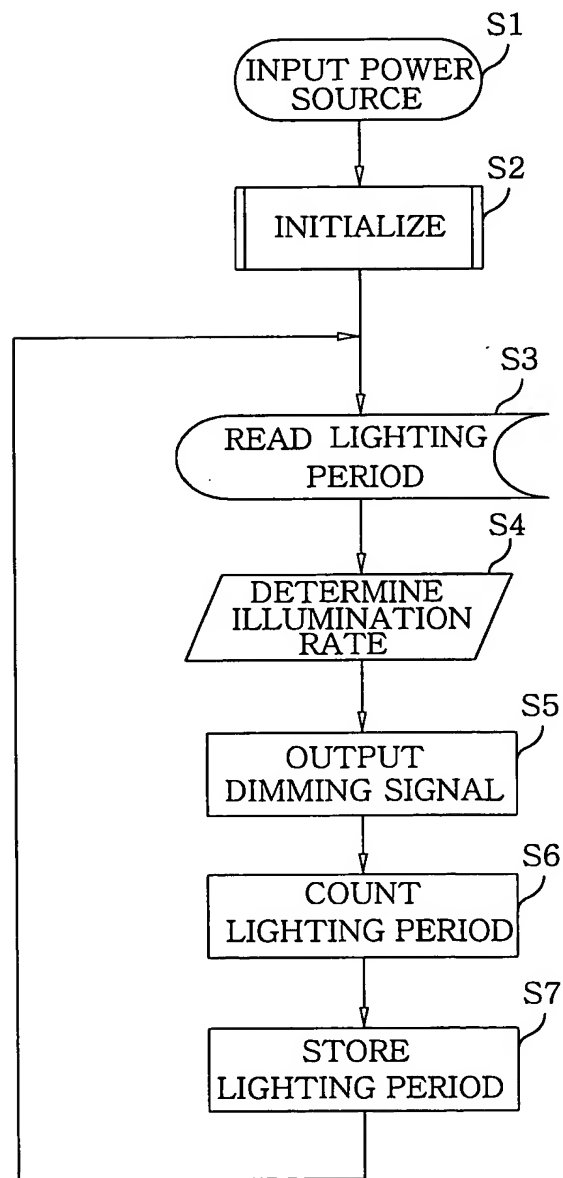


FIG. 46A

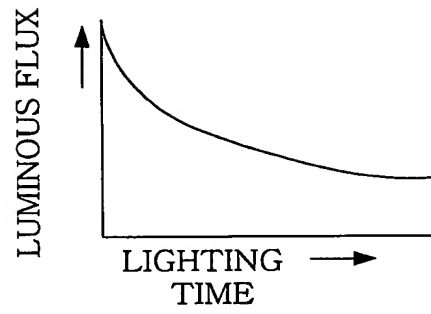


FIG. 46B

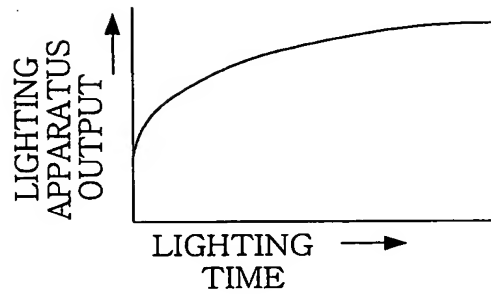


FIG. 46C

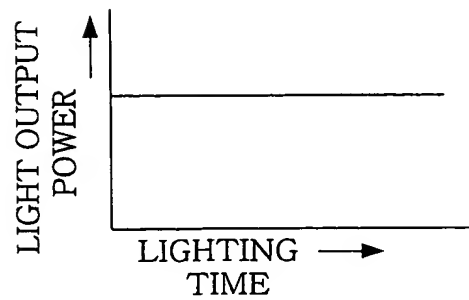


FIG. 47
(PRIOR ART)

